A GENERAL APPROACH FOR THE SYNTHESIS OF PETRI NET BASED LIVENESS ENFORCING SUPERVISORS IN FLEXIBLE MANUFACTURING SYSTEMS

by

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APPROVAL PAGE

This is to certify that I have read the thesis entitled "A General Approach for the Synthesis of Petri Net Based Liveness Enforcing Supervisors in Flexible Manufacturing Systems" by Umar Suleiman Abubakar and that in my opinion it is fully adequate, in scope and quality, as a thesis for the degree of Master of Science in Electrical and Computer Engineering, the Graduate Institute of Science and Engineering, Melikşah University.

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A General Approach for the Synthesis of Petri Net Based Liveness Enforcing Supervisors in Flexible Manufacturing Systems

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M.Sc. Thesis in Electrical and Computer Engineering

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ABSTRACT

In this thesis, a general approach is proposed for the computation of a liveness enforcing supervisor for the Petri net model of a flexible manufacturing system (FMS) prone to deadlocks. The proposed deadlock control policy in this thesis requires a modification to be made to the original Petri net model prone to deadlocks. The modification is simply the addition of a global sink/source place (GP), which is employed temporarily in the design process and then removed when the system becomes live. The proposed method is easy to use, straightforward and has computational simplicity. The applicability of the proposed approach is illustrated through examples from the related literature.

Keywords: Flexible Manufacturing Systems (FMS), Deadlock, Petri nets, liveness enforcing supervisor.

Esnek Üretim Sistemlerinde Petri Ağı Temelli Canlılık Uygulayıcı Gözeticilerin Sentezlenmesi için Genel Bir Yöntem

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ÖZ

Bu tezde, kördüğüm problemi olan bir esnek üretim sisteminin Petri ağı modeli için bir canlılık uygulayıcı gözetici hesaplanması konusunda genel bir yöntem önerilmektedir. Bu tezde önerilen kördüğüm kontrolu yaklaşımında, kördüğüm problemi olan asıl Petri ağı modelinde değişiklik yapılması söz konusudur. Buna göre, tasarım sürecinde geçici olarak kullanılan ve daha sonra asıl Petri ağı modeli canlı olduğunda kaldırılan küresel bir kaynak / yutak mevkisi (a global sink/source place GP) eklenmektedir. Önerilen yöntem basit, kullanımı ve hesaplaması kolaydır. Önerilen yaklaşımın uygulanabilirliği örneklerle gösterilmektedir.

Anahtar Kelimeler: Esnek Üretim Sistemleri (FMS), kördüğüm, Petri ağları, canlılık uygulayıcı gözetici.

DEDICATION

I dedicate this thesis to my late father Alhaji Suleiman Abubakar, who left this world while I was at the beginning of my undergraduate. May his soul rest in the perfect peace of Paradise.

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CHAPTER 1 INTRODUCTION

The advancement in science and technology has brought about complex man-made systems that pose real challenges to both their developers and users. Discrete event systems (DES) are examples of such systems. DESs are dynamic system models with state changes driven by asynchronous occurrences of individual events [1]. DESs are characterized by properties such as process synchronization, concurrent operations and conflicts or resource sharing [2]. Resource allocation systems (RAS) are common in DESs such as flexible manufacturing systems (FMS), workflow management systems and computer operating systems [3].

FMSs as the modern industrial systems are readily adaptable to changes whether predicted or unpredicted, in which machines are able to manufacture parts and have the ability to handle varying levels of production. FMSs allow equipment to be used for more than one purpose. Most FMSs are controlled by computer programs and therefore are automated systems. An FMS consists of a finite number of shared resources such as machines, automated guided vehicles, robots, and buffers. The main aim of FMSs is to offer the speed required to change with market conditions quickly [4].

In an FMS different jobs are carried out by different parts of the system concurrently while sharing limited resources of the system. A situation arises in which one part of the system is holding on some of the limited resources leaving other parts waiting indefinitely for the resources to be released. This situation leads to deadlocks in parts of the system or in the system as a whole. Deadlock in an RAS is a highly undesirable situation and must be dealt with in order to avoid catastrophic results since it affects the overall system's throughput and may lead to complete system failure or stoppage. There are four necessary conditions for the occurrence of deadlocks in RASs [5]. These conditions are given:

- 1) *Mutual Exclusion*: This means that two or more parts can acquire a resource at the same time. The resource is only exclusively occupied by one part of a system.
- 2) *No Preemption*: This implies that once a resource is acquired by a part in process it cannot be forcibly removed by any external agent.
- 3) *Hold and Wait*: This indicates that a process acquires some resources and awaits additional resources.
- 4) *Circular Wait*: This is a situation whereby there is a set of linearly ordering processes such that each process requests the resources currently held by the next process while the last process requests the resources held by the first.

Deadlock occurs if all of these conditions hold, and cannot happen if any of them does not hold [5]. In manufacturing systems, the first three conditions are already present. Thus, deadlock occurs when two or more jobs enter a circular wait state, and to avoid deadlock in manufacturing systems is to guarantee that there is no circular wait [6]. The first three conditions depend on the physical property of a system and its resources. However, the last is decided by the request, allocation and release of system resources. It is controllable and can be broken by properly assigning the resource of a system, aiming to avoid the occurrence of a circular wait [5].

As the scope of control theory is being extended into the fields of manufacturing systems, robotics, computer systems, communication networks and so on, there is need for different models capable of describing events that characterize the behaviors of these systems. The formal tools that are used to model such systems are finite state machines, graph theory, automata and Petri nets.

Finite state machines are conceptual models for discrete event systems (DESs). They consist of a finite number of states, transitions between these states and actions. States present certain behaviors. A transition indicates a state change and is guided by a condition. Graph theory can be used to describe interactions between activities or operations and resources from which a deadlock control policy can be derived. In graph theory frame work, deadlocks are always related with the circuits of the graph and their occurrences can be detected by simply computing all the circuits [5]. Automata are a powerful modeling tool for systems with an infinite number of states. They provide a comprehensive and

structural treatment of the modeling and control of discrete event systems. A number of deadlock control policies that are computationally efficient are developed based on automata [5]. The modeling tool that we use in this research is called Petri nets. Petri nets have been used widely by researchers to model, analyze, design and control FMSs because of their properties which are suitable to detect deadlocks. There are software packages available which make Petri nets easy to be used as systematic tool to model and handle the control of a system in the real world [5].

Several studies have been carried out in the last two decades to deal with the deadlock problems in RAS. Three approaches have been identified for this purpose. The first one is deadlock detection and recovery. This approach employs a mechanism that detects the occurrence of a deadlock in a system and then puts the system back to its deadlock free state. This approach does not eliminate the occurrence of deadlocks, but relies on its ability to handle them when they occur. The efficiency of this method depends on the response time of its implemented algorithms, [3] [7], [8]. The second approach is deadlock avoidance. This method keeps the system away from deadlocks by using a control policy that determines the correct system evaluations among the feasible ones. Even though this method improves system throughput and better utilization of system resources, it does not completely eliminate deadlocks. The third one is deadlock prevention [3], [8], [9]. This approach has received more attention and is a well-defined problem in DES. It is used at the stage of system design and planning, and therefore does not require run-time costs. A control policy is added to the system in such a way that deadlocks never happen in the system. The computation of this method is done off-line in a static way. Control places and related arcs are used for this purpose [3], [8]. Our research is based on the third approach.

Dealing with deadlocks is not an easy task, because there are three important criteria that are considered in evaluating the performance of a liveness-enforcing supervisor: behavioral permissiveness, structural complexity and computational complexity. A maximally permissive optimal supervisor can lead to high utilization of system resources, and a supervisor with a simple structure can reduce the hardware and software costs in the stage of verification, validation and implementation [9]. Researchers try their best to come up with deadlock prevention policies that meet these three criteria. Petri nets are used as mathematical tool to model, analyze and control the FMSs. In Petri net analysis, two techniques are used for deadlock prevention: structural analysis [10], [11], [12] and reachability graph analysis [13], [14]. In structural analysis technique, structural objects of Petri nets, such as siphons and resource transition circuits are used in developing deadlock prevention policies. The control laws are simple and the computational complexity is reduced. However, the technique suffers from structural complexity and the controlled systems obtained are often suboptimal. Details of research works and developments on this techniques, especially by using siphons, can be found in [3], [8], [10], [11], [12], [15], [16], [17], [18], [19], [20], [21].

The reachability graph (RG) analysis technique employs the behavior of a system from its generated RG. Though this analysis technique almost always gives a highly or even maximally permissive liveness-enforcing supervisor, it suffers from the state explosion problem. This is due to the fact that it requires generating all or a part of reachable markings. The theory of region was proposed which is an effective approach [22], [23]. The approach can definitely find an optimal supervisor if there is such a supervisor. The method has structural and computational problems. An important method was proposed in [13], where the RG of a Petri net model suffering from deadlocks is split into two parts: a live-zone (LZ) and a deadlock-zone (DZ). At each iteration, a first-met bad marking (FBM) derived from the reachability graph is selected and control place is designed to prevent the FBM from being reached. The design of the control place is done by using a place invariant (PI) based method proposed in [22]. An FBM is a marking in the DZ, presenting the very first entry from the LZ to the DZ. The drawback of the method is that it cannot guarantee the behavioral optimality of the supervisor, and it is easier to use for systems with a small reachable space.

Another method that combines markings and siphons was proposed in [25]. The method is a selective siphon control policy in which highly permissive behavior can be obtained by a small-sized supervisor. The policy was improved in [26] by avoiding a complete siphon enumeration. However, there is lack of formal proof to show that the policy is definitely maximally permissive in theory [5]. Other related works can be found in [2], [13], [14], [23], [25], [26], [27].

A divide-and-conquer strategy is another approach of liveness-enforcing supervisors which was claimed in [28] to be computationally superior compared with the well-established global-conquer approaches as in [13], [14]. In [29] a computationally efficient divide-and-conquer strategy for the computation of liveness-enforcing supervisors (LES) was presented. The method improves the conventional RG based methods. However, it is necessary to deal with too many submodels when the number of shared resources is big. Therefore the objective of this thesis is to propose a general approach for the computation of a liveness enforcing supervisors for the Petri net model of an FMS without dividing a given PN model into its submodels, and also without transformation or reduction of the given PN model as in [13], [14]. The proposed method is easy to apply and straight forward. The applicability of the proposed method is shown by examples.

The remainder of this thesis is organized as follows. Chapter 2 comprises of basic definitions of Petri net, equations for the computations of the monitors, redundancy check algorithm and simple FMS system and its Petri net model with its reachability graph. Chapter 3 explains the proposed method with an illustrative example to show how the method is applied on a simple Petri net model. Chapter 4 contains application examples of the proposed method on two different Petri net models. Finally Chapter 5 provides the conclusion of the thesis.

CHAPTER 2

PRELIMINARIES

2.1 INTRODUCTION

In this chapter some basic concepts related to this thesis are considered. These include Petri nets, computations of control places (monitors), a method used to identify and eliminate redundant monitors. Finally a simple FMS system and its Petri net model are explained.

2.2 PETRI NETS

Petri net were proposed by C. A. Petri in 1962 in his PhD thesis as net-like mathematical tool for the study of communication with automata [30]. Today, Petri nets are a powerful modeling formalism in many disciplines such as computer science, system engineering, communication and transport systems [31], [32]. Their further development was facilitated by the fact that they combine a well-defined mathematical theory with a graphical representation. Using Petri nets, it is possible to set up algebraic equations, state equations and other mathematical models describing the behavior of systems.

The following definitions are from [5], [9].

2.2.1 Definition 1 Petri net is a four-tuple defined as $N = (P, T, F, W)$, where P and T are finite non-empty and disjoint sets. *P* is a set of places and *T* is set of transitions with $P \cup T$ $\neq \emptyset$ and $P \cap T = \emptyset$. $F \subseteq (P \times T) \cup (T \times P)$ is called a flow relation of the net, it is represented by arrows from transitions to places or from places to transitions. *W:* $(P \times T) \cup (T \times P) \rightarrow$

ℕ is a mapping which assigns a weight to an arc: *W* (*x*, *y*) > 0 iff (*x*, *y*) ∈ *F*, and *W* (*x*, *y*) = 0, otherwise, where $x, y \in P \cup T$ and N is the set of non-negative integers.

2.2.2 Definition 2 A Petri net $N = (P, T, F, W)$ is called an ordinary net, denoted as $N = (P, T, F, W)$ *T*, *F*), if ∀ *f* ∈ *F*, *W* (*f*) = 1, and is called generalized if ∀ *f* ∈ *F*, *W* (*f*) > 1.

2.2.3 Definition 3 A node $x \in P \cup T$, $\cdot x = \{y \in P \cup T | (x, y) \in F\}$ is called the preset of x, while $x \bullet = \{y \in P \cup T | (x, y) \in F\}$ is called the post set of x.

2.2.4 Definition 4 A marking is a mapping *M: P* \rightarrow *N. M (p)* represents the number of tokens in a place *p*. Markings and vectors are usually described using a multiset or formal sum for space economy. $\sum_{p \in P} M(p)p$, is used to denote vector *M*. A marked Petri net is represented by a pair (N, M_0) .

2.2.5 Definition 5 A net is said to be pure (self-loop free) iff $\exists (x, y) \in (P \times T) \cup (T \times P)$: $(x, y) \in F \wedge (y, x) \in F$.

2.2.6 Definition 6 An incidence matrix of net *N* is a $|P| \times |T|$ integer matrix with [*N*] (*p*, $t) = W(t, p) - W(p, t).$

2.2.7 Definition 7 A transition is fired or enabled at marking *M* if $\forall p \in \mathcal{F}$, *M* (*p*) ≥ *W* (*p*, *t*). This fact is denoted as *M* [*t*). Firing a transition yields a new marking *M* ' such that $\forall p \in$ *P*, *M* ['] (*p*) = *M* (*p*) - *W* (*p, t*) + *W* (*t, p*), denoted by *M* [*t*) *M* ', and *M* ' is called an immediately reachable marking from *M*. *M* ' is reachable form *M* if there exists a sequence of transitions $\sigma = t_1, t_2, \ldots, t_n$ and markings M_1, M_2, \ldots , and M_{n-1} such that $M[t_1) M_1[t_2] M_2$ $[t_3) \ldots M_{n-1}$ $[t_n)$ *M* $'$ holds and satisfies the state equation $M' = M + [N] \overrightarrow{\sigma}$, where $\overrightarrow{\sigma}$: $T \rightarrow \mathbb{N}$ is a vector of non-negative integers called a counting vector, and $\vec{\sigma}(t)$ indicates the algebraic sum of all occurrences of *t* in σ . *M* [) is the set of all markings reachable from *M* by enabling any possible sequence of transitions. M_0 [) is called the set of reachable markings of a Petri net *N* from initial marking M_0 , often denoted by R (*N, M*₀). R (*N, M*₀) can be graphically expressed by a reachability graph of a net (N, M_0) which is denoted as $G(N, M_0)$. $G(N, M_0)$ is a directed graph which has its nodes in $R(N, M_0)$ as markings, and its arcs are labeled by the transitions of *N*. An arc from M_1 to M_2 is labeled by the *t* if M_1 [*t*) M_2 .

2.2.8 Definition 8 A place is called *k*-bounded ($k \in \mathbb{N} \setminus \{0\}$) if $\forall M \in R$ (*N, M*₀): *M* (*p*) \leq *k*. A net is *k*-bounded if every place is *k*-bounded. A net is safe if it is 1-bounded.

2.2.9 Definition 9 Given a net system (N, M_0) with $N = (P, T, F, W)$, a transition $t \in T$ is live at *M*^o if ∀ *M* ∈ *R* (*N, M*^o), ∃*M* ′ ∈ *R* (*N, M*), *M* ′ [*t*) holds. A transition is said to be live if it is potentially firable in any marking $R(N, M)$, and the net system (N, M_0) is said to be live. A transition *t* ∈ *T* is dead at *M* if $\sharp M' \in R$ (*N, M*) such that *M'* [*t*) holds, and the transition cannot fire any more. A net system (N, M_0) with a dead transition is said to be prone to deadlock since it has a transition which is not potentially firable. (N, M_0) is deadlockfree if $\forall M \in R (N, M_0), \exists t \in T, M[t]$ holds.

2.2.10 Definition 10 A Petri net is said to be conservative if the total number of tokens of all its reachable markings is constant.

2.3 COMPUTATION OF MONITORS

In the place invariant method proposed in [24] for the computation of monitors, the controlled Petri net of a system has an incidence matrix *D* made up of both the original Petri net and the added control places with their related arcs. The idea is to force the system to obey constraints which can be grouped in matrix form as follows:

$$
L\mu_{\rm p} \leq b \tag{1}
$$

Where: μ_p is the marking vector of the Petri net model (PNM), *L* is an $n_c \times n$ integer matrix representing the place invariant, b is an $n_c \times 1$ integer vector and n_c is the number of constraints of (1). If a non-negative slack variable μ_c is introduced, the inequality constraint becomes equality as follows:

$$
L\mu_{\rm p} + \mu_{\rm c} = b \tag{2}
$$

 μ_c is an $n_c \times 1$ integer vector, representing the markings of the control places.

If the incidence matrix of the PNM is given as D_P , the Petri net controller D_c , which is a row vector representing the connection between the control places and the transition can be defined as follows:

$$
D_c = -LD_P \tag{3}
$$

The initial marking of the controlled PNM μ_{c0} , which is computed in such a way that the place invariant (PI) of equation (2) is initially satisfied, is given as follows:

$$
\mu_{\rm c0} = b - L \mu_{\rm P0} \tag{4}
$$

When dealing with large Petri nets, the incidence matrices tend to be very big, and this is a major drawback of this method. However, a simplified method was proposed in [14] in order to reduce the size of the incidence matrix *DP*. Equations 2 and 3 are modified in [14], and there is no need to use the incidence matrix D_P , the computation is done by using the incidence matrix *DPI* of PI related Petri net.

$$
D_{\rm c} = -L_{\rm PI} D_{\rm PI} \tag{5}
$$

Where D_{PI} is the incidence matrix of the PI related net with *j* places and *k* transitions, L_{PI} is a $j \times l$ integer row vector representing the invariant related places, D_c is a $k \times l$ integer row vector representing the incidence matrix of the monitor.

$$
\mu_{\rm c0} = b - L_{PI} \mu_{PI0} \tag{6}
$$

Where L_{PI} is place invariant related integer vector, μ_{PI0} is initial marking of place invariant related net.

By definition, initially there is no tokens within the activity places, which means that $L_{PI} \mu_{PIO} = 0$. Therefore equation (6) becomes:

$$
\mu_{c0} = b \tag{7}
$$

2.4 IDENTIFICATION AND ELIMINATION OF REDUNDANT MONITORS

There are may exist some redundant monitors among the monitors or control places (CPs) computed for liveness enforcing supervisor. If redundant CPs are removed from a live Petri net model (LPN), the Petri net model still maintains its liveness. Removing redundant CPs from a LPN model and leaving only the necessary ones may reduce the structural complexity of the LPN model. Below are redundancy test algorithms proposed in [33] for removing redundant monitors from a LPN model.

Algorithm Redundancy Test: Redundancy test for LES of FMS

- **Input**: A live Petri net (*LPN*) model, denoted by a net system (*N*₀*, M*₀), of an FMS, controlled by *n CP*s; $CP = \{C_1, C_2, ..., C_n\};$
	- (1) [Define] β_0 : the number of reachable markings or states of reachability graph (R_0) of (N_0, M_0) .

[Define for Algorithm A] *β*A: the number of reachable markings or states of *R^A* of $(N_A, M_A); n = j + k$, where *n*: the number of *CPs* of *LPN; j:* the number of redundant *CP*s; *k*: the number necessary *CP*s:

[Define for Algorithm B] β_B : the number of reachable markings or states of R_B of (N_B) M_B ; $n = l + m$, where *n*: the number *CP*s of *LPN*; *l*: the number of redundant *CPs*; *m*: the number of necessary *CP*s;

- (2) Apply Algorithm A to (N_0, M_0) and the resultant net system is denoted as (N_A, M_A) .
- (3) Apply Algorithm B to (N_0, M_0) and the resultant net system is denoted as (N_B, M_B) .

Output: *If* $(j > 0)$ [for Algorithm A]

then Output A = an *LPN*, denoted a net system (N_A, M_A) , controlled by k necessary *CP*s; there are *j* redundant *CP*s;

if $\beta_A = \beta_0$ then the controlled behaviour of (N_A , M_A) is the same as (N_0 , M_0) *if* $\beta_A > \beta_0$ then the controlled behaviour of (N_A , M_A) is more permissive than (N_0, M_0)

else there is no redundant *CP*s obtained due to Algorithm A and therefore for Algorithm A: Output = Input;

If $(l > 0)$ [for Algorithm B]

Then Output B = an *LPN*, denoted by a net system (N_B, M_B) , controlled by *m* necessary *CP*s; there are *l* redundant *CP*s;

if $\beta_B = \beta_0$ then the controlled behaviour of (N_B , M_B) is the same as (N_0 , M_0) $if \beta_B > \beta_0$ then the controlled behaviour of (N_B *, M_B*) is more permissive than (N_0, M_0)

else there is no redundant *CP*s obtained due to Algorithm B and therefore for Algorithm B: Output = Input;

End of Algorithm Redundancy Test

Algorithm A: Front-to-Back (*FTB*) redundancy test for *LES* of FMS.

Input: A live Petri net (*LPN*) model, denoted by a net system (*N*₀*, M*₀), of an FMS, controlled by

 $n \text{ } CPs$; $CP = \{C_1, C_2, ..., C_n\}$;

- (1) [Initialize] $N_A := N_0$; $M_A := M_0$; $i = 1$; $j = 0$; $k = 0$;
- (2) Remove C_i from (N_A, M_A) . Denoted the resultant net system by (N_i, M_i) .
- (3) Check the liveness property of (N_i, M_i) , compute the reachability graph (R_i) of C_i and define *βAi*, i.e., the number of reachable markings of *Ri; If* (N_i, M_i) is NOT LIVE

then put C_i back into (N_i, M_i) ; $k = k + 1$; which means that C_i is necessary to keep the PN model live.

else [i.e. *If* (N_i, M_i) is live], $j = j + 1$; which means that C_i is redundant,

if $\beta_{Ai} = \beta_0$ then the controlled behaviour of (*N*_i, *M*_{*i*}) is the same as (*N*₀, *M*₀)

if $\beta_{Ai} > \beta_0$ then the controlled behaviour of (*N*_i, *M_i*) is more permissive than (*N*₀, *M*₀) *endif*

- (4) $N_A := N_i$; $M_A := M_i$
- (5) $i = i + 1$
- (6) *if* $i \leq n$ *then* go to step 2.

Output: *If* $(j > 0)$

then Output = an *LPN*, denoted by a net system (N_A, M_A) , controlled by *k* necessary *CP*s;

there are *j* redundant *CP*s;

if $\beta_A = \beta_0$ *then* the controlled behaviour of (N_A , M_A) is the same as (N_0 , M_0)

if $\beta_A > \beta_0$ *then* the controlled behaviour of (N_A , M_A) is more permissive than (N_0 , M_0)

 else there is no redundant *CP*s and therefore Output = Input;

End of Algorithm A

Algorithm B: Back-to-Front (*BTF*) redundancy test for *LES* of FMS.

Input: A live Petri net (*LPN*) model, denoted by a net system (N_0 , M_0), of an FMS, controlled by

 $n \text{ } CPs$; $CP = \{C_1, C_2, ..., C_n\}$;

- (1) [Initialize] $N_B := N_0$; $M_B := M_0$; $i = 1$; $l = 0$; $m = 0$;
- (2) Remove C_i from (N_B, M_B) . Denoted the resultant net system by (N_i, M_i) .
- (3) Check the liveness property of (N_i, M_i) , compute the reachability graph (R_i) of C_i and define *βBi*, i.e., the number of reachable markings of *Ri;*

If (N_i, M_i) is NOT LIVE

then put C_i back into (N_i, M_i) ; $m = m + 1$; which means that C_i is necessary to keep the PN model live.

else [i.e. *If* (N_i, M_i) is live], $l = l + 1$; which means that C_i is redundant,

if $\beta_{Bi} = \beta_0$ then the controlled behaviour of (*N*_i, *M*_i) is the same as (*N*₀, *M*₀)

 $if \beta_{Bi} > \beta_0$ then the controlled behaviour of (*N*_i, *M*_i) is more permissive than (*N*₀, *M*₀) *endif*

$$
(4) N_B := N_i ; M_B := M_i
$$

(5)
$$
i = i - 1
$$

(6) *if* $i \neq 0$ *then* go to step 2.

Output: *If* $(l > 0)$

then Output = an *LPN*, denoted by a net system (N_B, M_B) , controlled by *m* necessary *CP*s;

there are *j* redundant *CP*s;

if $\beta_B = \beta_0$ *then* the controlled behaviour of (N_B , M_B) is the same as (N_0 , M_0) $if \beta_B > \beta_0$ *then* the controlled behaviour of (N_B , M_B) is more permissive than (N_0 , M_0) *else* there is no redundant *CP*s and therefore Output = Input;

End of Algorithm B

Redundancy test algorithm makes use of both Algorithm A and B. Algorithm A test each CP from number 1 to end, while B tests each CP starting from end to number 1. Both tests may produce the same result or it may be possible to obtain different results [33].

2.5 FMS EXAMPLE

In this section an example of modeling of an FMS and the computation of its reachability graph is considered. Fig. 2.1 shows an FMS from [13] consisting of two machines M1 and M2 each of which can process one part at a time and one robot which can hold one part at a time. The FMS has two input/output buffers, I/O1 and I/O2 through which parts enter the FMS. We consider only two parts: P1 and P2. It is assumed that there are no parts initially in the system [14]. The production sequences are as follows:

Figure 2.1. An example FMS.

Fig. 2.2 is the PNM of the FMS depicted in Fig. 2.1. There are eleven places in the PNM, $P = \{p1-p6, p11-p13, p21-p22\}$ and eight transitions, $T = \{t1-t8\}$. There are six activity places, $P_A = \{p1-p6\}$, which represent the operations of activities of M1, R and M2, and M2, R and M1 for the part type P1and P2 respectively, three resource places $P_R = \{p11-p13\}$ and two sink/source places $P_A = \{p21-p22\}$. The number of tokens in the sink/source places, p21 and p22 represent the number of concurrent activities that can take place for part types P1 and P2 respectively. The initial marking of p12 is one, as robot R can hold one part at a time. Similarly the initial markings of p11 and p13 are all one as machines can process one part at a time.

From the reachability graph (RG) of the PNM, it can be verified that the uncontrolled PNM is prone to deadlocks. There are 20 states within RG, 5 of which are bad states, in the dead zone (DZ) and 15 of which are good states within the live zone (LZ). Therefore a control policy is required to prevent these 5 states within the DZ from being reached in order to get the live PNM.

Figure 2.2. Petri net Model (PNM) of the FMS for the two production sequences.

The reachability graph of the FMS is shown in Fig. 2.3 indicating the 20 states within the RG. States 8 and 13 are deadlock states while states 10, 11 and 12 are bad states leading to the deadlock states. These five states form the DZ while the remaining 15 states are good sates; within the LZ.

Figure 2.3. The RG of the PNM.

The markings of the states of the RG are shown in Table 2.1.

Twore 2.1. Brates of the compared NO of the Fight.					
$s_1 = p_{11} + p_{12} + p_{13} + 3p_{21} + 3p_{22}$	$s_{11} = p_1 + p_4 + p_{12} + 2p_{21} + 2p_{22}$				
$s_2 = p_1 + p_{12} + p_{13} + 2p_{21} + 3p_{22}$	$s_{12} = p_1 + p_5 + p_{13} + 2p_{21} + 2p_{22}$				
$s_3 = p_2 + p_{11} + p_{13} + 2p_{21} + 3p_{22}$	$s_{13} = p_1 + p_4 + p_5 + 2p_{21} + p_{22}$				
$s_4 = p_1 + p_2 + p_{13} + p_{21} + 3p_{22}$	$s_{14} = p_4 + p_{11} + p_{12} + 3p_{21} + 2p_{22}$				
$s_5 = p_1 + p_3 + p_{12} + p_{21} + 3p_{22}$	$s_{15} = p_5 + p_{11} + p_{13} + 3p_{21} + 2p_{22}$				
$s_6 = p_2 + p_3 + p_{11} + p_{21} + 3p_{22}$	$s_{16} = p_4 + p_5 + p_{11} + 3p_{21} + p_{22}$				
$s_7 = p_1 + p_2 + p_3 + 3p_{22}$	$s_{17} = p_4 + p_6 + p_{12} + 3p_{21} + p_{22}$				
$s_8 = p_1 + p_2 + p_4 + p_{21} + 2p_{22}$	$s_{18} = p_5 + p_6 + p_{13} + 3p_{21} + p_{22}$				
$s_9 = p_3 + p_{11} + p_{12} + 2p_{21} + 3p_{22}$	$s_{19} = p_4 + p_5 + p_6 + 3p_{21}$				
$s_{10} = p_2 + p_4 + p_{11} + 2p_{21} + 2p_{22}$	$s_{20} = p_6 + p_{12} + p_{13} + 3p_{21} + 2p_{22}.$				

Table 2.1. States of the computed RG of the PNM.

CHAPTER 3

THE PROPOSED METHOD AND ITS ALGORITHM

3.1 INTRODUCTION

In this chapter, a new approach for synthesis of Petri net based liveness enforcing supervisors in FMS is proposed. In the proposed method, reachability graph (RG) is used for tackling deadlock problems of a given Petri net model (PNM). The RG of a PNM suffering from deadlocks has two partitions: dead zone (DZ) and live zone (LZ). The DZ comprises of deadlock states together with bad states leading deadlock states while the LZ has the good states. The aim of this method is to prevent a PNM of an FMS from reaching all states within DZ while allowing every state within LZ to be reached.

There are three categories of places in a PNM of an FMS: *resource places* PR, *activity (operational) places* PA, and *sink/source places* PS/S. Resource places represent the shared/non-shared resources. Activity (operational) places represent an action to process a part in a production sequence. The number of tokens initially deposited into sink/source places represent the number of the concurrent activities which can take place in a production sequence [27]. In this proposed method, we only focus on the markings of activity places when the RG of a PNM is computed. We consider only the activity places which have tokens.

3.2 THE PROPOSED METHOD ALGORITHM

The method proposed employs a global sink/source place (GP) in computing the liveness enforcing supervisors in an iterative way. In this control policy the reachability graph (RG) of the given PNM is generated by a Petri net analysis tool called INA [34], which gives both the LZ as the first strongly connected components, and the DZ, as the strongly connected components other than the first one of a given PNM. At each iteration, starting from one token, the number of tokens in the GP is increased by one and the RG of the net is computed. If the net is live, the number of tokens in the GP is increased by one and the RG is computed again. When the net is not live, the RG of the related net is divided into a DZ and a LZ. The latter constitutes the good states of the RG which represents optimal solution. The objective here is to prevent all states within the DZ from being reached, because they are considered as bad markings (BM). From a BM we consider only the markings of the activity places. A monitor (control place) with its related arcs and initial marking is computed to prevent the BM from being reached [9], [14], by means of a place invariant (PI). Computed PI is implemented in such a way that the sum of tokens within the subset of the activity places must be at most one token less than their current number.

After the PNM becomes live, a redundancy test as proposed in [33] is carried out to remove any redundant monitor from the computed monitors. Finally, a live controlled Petri net model with all necessary control places as liveness enforcing supervisor is obtained. The proposed method provides optimal permissiveness on some Petri net models and near optimal on others. The method is straight forward and easy to use. The algorithm of the proposed deadlock prevention policy is as follows.

Algorithm: Synthesis of a liveness enforcing supervisor by means of a global sink/source place (GP)

Input: A Petri net model (PNM) of an FMS prone to deadlocks.

Output: A live controlled Petri net model.

Step 1: Identify the input and output transitions of all sink/source places $P_{S/S}$ and use them for adding a global sink/source place (GP) to the PNM. The addition of the GP will be made in such a way that its input transitions are input transitions of all $P_{S/S}$ and its output transitions are output transitions of all P_{S/S}. The resultant net system is $PNM_B = PNM + GP.$

Step 2: for $(B = 1; B \le k; B++)$

 $/* B$ is the number of tokens in the GP, k is the sum of initial tokens in all sink/source Places */

{

 $2.B.1: Compute the RG_B of the PNM_B,$

if PNM_B is live, then consider a new net, i.e. go to Step 2.B.1 else compute the LZ_B and DZ_B of the RG_B endif

2.B.2: From each BM of DZ_B , define a place-invariant PI.

2.B.3: Compute a monitor C, for each PI using the simplified invariant-based method [13].

2.B.4: If there are more than one monitor computed for PNM_B then carry out the redundancy test to eliminate any redundant monitors by using the method [33].

2.B.5: Add necessary monitors computed in the previous step within PNM_B (PNM_B) $:= PNM_B + computed \text{ monitored}$ }

Step 3: Obtain the live controlled PNM by adding all the necessary monitors computed in Step 2 within the PNM.

Step 4: Exit

End of Algorithm

3.3 ILLUSTRATIVE EXAMPLE

The success of any algorithm for solving a problem is measured by its ability to work on an example and also its ability to generalize on other examples that it has yet to see. If the method or algorithm works on every example, then we can claim its success since it is generalized. In this section, an example of synthesizing liveness enforcing supervisor based on the method proposed in the previous section on a simple uncontrolled PNM of an FMS from $[10]$ is considered. The PNM shown in Fig. 3.1 is an S^3PR (a System of Simple Sequential Process with Resources) model. It is verified that there are 95 states in the RG, of which 11 states are within DZ, representing the bad marking to be dealt with, and 84 states are in the LZ, representing good states or legal markings. The objective here, by applying the proposed method, is to obtain a live PNM while preventing the 11 bad states from being reached.

Figure 3.1. S^3PR Petri net model of an FMS from [10].

In the PNM there are six activity places $P_A = \{p2-p4, p6-p8\}$, three shared resource places $P_R = \{p9-p11\}$, and two sink/source places $P_{S/S} = \{p1, p5\}$.

Step 1: The input and output transitions of GP are $G = \{t4, t5\}$ and $GP = \{t1, t8\}$ respectively. The new Petri net model obtained with the addition of the GP, $PMM_B = PMM_B$ + GP is shown in Fig. 3.2.

Figure 3.2. Net; $PNM_B = PNM + GP$.

Step 2: for $(B = 1; B \le 5; B++)$

Step 2.1.1: $(B = 1)$, when one token is deposited in the GP, as shown in Fig. 3.3, the net PNM₁ is live with 7 good states. B: = B++ (B = 2).

Figure 3.3. Live PNM₁ with 7 good states.

Step 2.2.1: $(B = 2)$, When two tokens are deposited in the GP, the net PNM₂ is obtained as shown in Fig. 3.4. The PNM₂ is live with 25 good states. B: = B++ (B = 3).

Figure 3.4. Live PNM₂ with 25 good states.

Step 2.3.1: $(B = 3)$, the net PNM₃, shown in Fig. 3.5, is not live. The reachability graph RG₃ computed for the PNM₃ has 53 good states in the LZ_3 and 2 bad states BM₁ and BM₂ within the DZ₃.

Figure 3.5. The PNM3.

Step 2.3.2: The markings of activity places of $BM₁$ and $BM₂$ are shown in Table 3.1

State nr.		'n	'n	

Table 3.1. The markings of activity places of $BM₁$ and $BM₂$.

The place invariants PI_1 and PI_2 for the BM_1 and BM_2 respectively are:

 $PI_1 = \mu_2 + \mu_7 \leq 2$

 $PI_2 = \mu_3 + \mu_8 \leq 2$

Step 2.3.3: The computation of the monitors C_1 and C_2 are carried out as follows:

 p2 p7 $L_{PII} = [1 \ 1]$

t1 t2 t6 t7
\n
$$
D_{PI1} = \begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & -1 & 1 \end{bmatrix} p^2
$$
\n
$$
D_{C1} = - L_{PI1}. D_{PI1} = - \begin{bmatrix} 1 & 1 \end{bmatrix} \begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & -1 & 1 \end{bmatrix}
$$
\n
$$
D_{C1} = \begin{bmatrix} 1 & -1 & -1 & 1 \end{bmatrix}
$$
\n
$$
t1 t2 t6 t7
$$
\nTherefore,
$$
D_{C1} = \begin{bmatrix} -1 & 1 & 1 & -1 \end{bmatrix}
$$
\n
$$
\mu_{0(c1)} = 2
$$
\n
$$
p^3 p^8
$$
\n
$$
L_{PI2} = \begin{bmatrix} 1 & 1 \end{bmatrix}
$$
\n
$$
t2 t3 t7 t8
$$
\n
$$
D_{PI2} = \begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & -1 & 1 \end{bmatrix} p^3
$$
\n
$$
D_{C2} = - L_{PI2}. D_{PI2} = - \begin{bmatrix} 1 & 1 \end{bmatrix} \begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & -1 & 1 \end{bmatrix}
$$
\n
$$
D_{C2} = - \begin{bmatrix} 1 & -1 & -1 & 1 \end{bmatrix}
$$
\n
$$
t2 t3 t7 t8
$$
\nTherefore,
$$
D_{C2} = \begin{bmatrix} -1 & 1 & 1 & -1 \end{bmatrix}
$$
\n
$$
\mu_{0(c2)} = 2
$$

The computed monitors are shown in Table 3.2.

rable 5.2. Monitors C_1 and C_2 .						
	'C:	($\mu_{0(c i)}$			
	t2, t6	t1, t7				
	t3, t7	t2, t8				

Table 3.2. Monitors C_1 and C_2

Step 2.3.4: Redundancy test carried out shows that both C_1 and C_2 are necessary.

Step 2.3.5: The controlled PNM₃ := PNM₃ + C₁ + C₂ is shown in Fig. 3.6. It is live with 53 good states. This is the optimal live behavior for the controlled PNM3.

Figure 3.6. The controlled $PNM_3 := PNM_3 + C_1 + C_2$.

- $B := B ++(B = 4).$
- Step: 2.4.1: $(B = 4)$, the net PNM₄, shown in Fig. 3.7, is not live. The reachability graph RG₄ computed for PNM⁴ has 77 good states in LZ⁴ and 1 bad state BM⁴ within DZ4.

Figure 3.7. The PNM₄.

Step 2.4.2: The markings of the activity places BM₃ are shown in Table 3.3.

Table 5.5. The markings of the activity places of Divis.													
State nr.			p4	p6		p8							

Table 3.3. The markings of the activity places of BM3.

The place invariant for the BM₃ is $PI_3 = \mu_2 + \mu_8 \le 3$.

Step 2.4.3: The computation of the monitors C_3 is carried out as follows:

$$
p2 \t p8
$$

\n
$$
L_{PI3} = \begin{bmatrix} 1 & 1 \end{bmatrix}
$$

\n
$$
t1 \t t2 \t t7 \t t8
$$

\n
$$
D_{PI3} = \begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & -1 & 1 \end{bmatrix} \begin{bmatrix} p2 \\ p8 \end{bmatrix}
$$

\n
$$
D_{C3} = - L_{PI3} \cdot D_{PI3} = - \begin{bmatrix} 1 & 1 \end{bmatrix} \begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & -1 & 1 \end{bmatrix}
$$

\n
$$
D_{C3} = - \begin{bmatrix} 1 & -1 & -1 & 1 \end{bmatrix}
$$

 t1 t2 t7 t8 Therefore, $D_{C3} = [-1 \ 1 \ 1 \ -1]$

 $\mu_{0(c3)} = 3$

The computed monitor is shown in Table 3.4.

Table 3.4. Monitor C_3 .												
			$\mu_{0(c_i)}$									
Ċ٩	t2. t7	t1, t8										

Step 2.4.4: Since there is only one monitor computed, the redundancy test is not necessary.

Step 2.4.5: The controlled PNM₄ is obtained by adding C_3 within the uncontrolled PNM₄ (PNM₄ := PNM₄ + C₃) as shown in Fig. 3.8. It is live with 76 good states. This is the optimal live behavior for the controlled PNM4.

Figure 3.8. The controlled PNM_4 ($PNM_4 := PNM_4 + C_3$).

 $B := B + (B = 5)$.

Figure 3.9. Live PNM₅.

Step 3: The design procedure applied in Step 2 is provided in Table 3.6. When the computed necessary monitors are added in the uncontrolled PNM, the controlled PNM is obtained as shown Fig. 3.10. It is verified that this controlled model is live with 84 good states.

Step 4: Exit.

Figure 3.10. The optimally controlled PNM.

		$\mu_{0(ci)}$
t2, t6		
t3, t7	t2, t8	

Table 3.6. The liveness enforcing procedure applied to the S^3 PR model.

CHAPTER 4

APPLICATION EXAMPLES

In this chapter, the proposed method is applied to obtain liveness enforcing supervisor for two Petri net models; an S³PR PNM from [13] and an AEMG PNM from [34]. Although the method gives optimal solution to the PNM treated in the illustrative example in chapter 3, in the examples considered in this chapter, for both cases liveness with near optimal permissiveness is achieved.

4.1 AN S ³PR PETRI NET EXAMPLE

The PNM of an FMS shown in Fig. 4.1 from [13], suffers from deadlock. It has 26,750 states within the RG, 21,581 of which are in the LZ while remaining 5,169 states are in the DZ.

Figure 4.1. S^3 PR Petri net model of an FMS from [13].

There are sixteen activity places $P_A = \{p2-p4, p6-p13, p15-p19\}$, seven shared resource places $P_R = \{p20-p26\}$, and three sink/source places $P_{S/S} = \{p1, p5, p14\}$.

Step 1: The input and output transitions of GP are $GP = \{t6, t14, t20\}$ and $GP = \{t1, t11, t20\}$ t15} respectively. The new Petri net model obtained with the addition of the GP, $PNM_B =$ $PNM_B + GP$ is shown in Fig. 4.2.

Figure 4.2. Net; $PNM_B = PNM + GP$.

Step 2: for $(B = 1; B \le 11; B++)$

Step 2.1.1: $(B = 1)$, when one token is deposited in the GP, as shown in Fig. 4.3, the net PNM₁ is live with 17 good states. $B: = B++ (B = 2)$.

Figure 4.3. Live PNM₁ with 17 good states.

Step 2.2.1: $(B = 2)$, when two tokens are deposited in the GP, as shown in Figure 4.4, the net PNM₂ is live with 132 good states. B: = B++ (B = 3).

Figure 4.4. Live PNM₂ with 132 good states.

Step 2.3.1: $(B = 3)$, when three tokens are deposited in the GP, as shown in Fig. 4.5, the net PNM₃ is not live. The reachability graph RG₃ computed for PNM₃ has 632 good states in the LZ₃ and 5 bad markings, BM_2 , BM_3 , BM_4 and BM_5 within the DZ₃.

Figure 4.5.The PNM3.

Step 2.3.2: The markings of activity places of BM1, …, BM5 are shown in Table 4.1.

. ---------- <i>o-</i> $- - - -$ $-$ - $-$ 1, \cdot \cdot , $-$ - $ \cdot$																
State	p	D		n	D	p	D	D	p	p					р	p
nr.	∠	2 ر	4	O		8	Q	10		12 ┸ 	13	15	16	7	18	19
279	0	O				θ				U		U	U	υ		
292	0	U				θ			←				U			
360	θ	$\bf{0}$				θ							0	v		
425	$\boldsymbol{0}$												0			
433						0										

Table 4.1. The markings of the activity places of $BM₁, ..., BM5$.

The place invariants PI_1 , PI_2 , PI_3 , PI_4 and PI_5 for BM_1 , BM_2 , BM_3 , BM_4 and BM_5 respectively are:

 $PI_1 = \mu_{13} + \mu_{19} \leq 2$ $PI_2 = \mu_{11} + \mu_{17} \leq 2$ $PI_3 = \mu_{12} + \mu_{18} \leq 2$ $PI_4 = \mu_3 + \mu_8 \leq 2$ $PI_5 = \mu_2 + \mu_3 \leq 2$

Step 2.3.3: The computation of the monitors C_1 , C_2 , C_3 , C_4 and C_5 are carried out as follows:

 p13 p19 $L_{PII} = [1 \ 1]$ t9 t10 t15 t16 $D_{PI1} = \begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & 1 & 1 \end{bmatrix}$ $\begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & 1 & -1 \end{bmatrix} \begin{bmatrix} p13 \\ p19 \end{bmatrix}$ $D_{C1} = -L_{PII} \cdot D_{PII} = -[1 \quad 1] \begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & 1 & 1 \end{bmatrix}$ $\begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & 1 & -1 \end{bmatrix}$ D_{C1} = $-[1 -1 1 -1]$ t9 t10 t15 t16 Therefore, $D_{C1} = [-1 \ 1 \ -1 \ 1]$ $\mu_{0(c1)} = 2$ p11 p17 $L_{PI2} = [1 \ 1]$ t7 t8 t17 t18 $D_{PL} = \begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & 1 & 1 \end{bmatrix}$ $\begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & 1 & -1 \end{bmatrix} \begin{bmatrix} p11 \\ p17 \end{bmatrix}$ $D_{C2} = -L_{P12} \cdot D_{P12} = -\begin{bmatrix} 1 & 1 \end{bmatrix} \begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & 1 & 1 \end{bmatrix}$ $\begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & 1 & -1 \end{bmatrix}$ $D_{C2} = - [1 \t -1 \t 1 \t -1]$

 t7 t8 t17 t18 Therefore, $D_{C2} = [-1 \ 1 \ -1 \ 1]$ $\mu_{0(c2)} = 2$ p12 p18 $L_{PI3} = [1 \ 1]$ t8 t9 t16 t17 $D_{PI3} \begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & 1 & 1 \end{bmatrix}$ $\begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & 1 & -1 \end{bmatrix} \begin{bmatrix} p12 \\ p18 \end{bmatrix}$ $D_{C3} = -L_{PI3}$ *. D_{PI3}* = $-$ [1 1] $\begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & 1 & 1 \end{bmatrix}$ $\begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & 1 & -1 \end{bmatrix}$ D_{C3} = $-[1 \t-1 \t1 \t-1]$ t8 t9 t16 t17 Therefore, $D_{C3} = \begin{bmatrix} -1 & 1 & -1 & 1 \end{bmatrix}$ $\mu_{0(c3)} = 2$ p3 p8 $L_{PI4} = [1 \ 1]$ t12 t13 t3 t4 $D_{P14} = \begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & 1 & 1 \end{bmatrix}$ $\begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & 1 & -1 \end{bmatrix} \begin{bmatrix} p3 \\ p8 \end{bmatrix}$ $D_{C4} = -L_{P14}$ *. D_{PI4}* = $-$ [1 1] $\begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & 1 & 1 \end{bmatrix}$ $\begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & 1 & -1 \end{bmatrix}$ D_{C4} = $-[1 \t-1 \t1 \t-1]$ t12 t13 t3 t4 Therefore, $D_{C4} = \begin{bmatrix} -1 & 1 & -1 & 1 \end{bmatrix}$ $\mu_{0(c4)} = 2$ p2 p3 $L_{PI5} = [1 \ 1]$

t11 t12 t13
\n
$$
D_{PI5} = \begin{bmatrix} 1 & -1 & 0 \\ 0 & 1 & -1 \end{bmatrix} \begin{bmatrix} p2 \\ p3 \end{bmatrix}
$$
\n
$$
D_{CS} = -L_{PI5} \cdot D_{PI5} = -\begin{bmatrix} 1 & 1 \end{bmatrix} \begin{bmatrix} 1 & -1 & 0 \\ 0 & 1 & -1 \end{bmatrix}
$$
\n
$$
D_{C3} = -\begin{bmatrix} 1 & 0 & -1 \end{bmatrix}
$$
\nt11 t12 t13
\nTherefore, $D_{CS} = \begin{bmatrix} -1 & 0 & 1 \end{bmatrix}$
\n $\mu_{0(c5)} = 2$

The computed monitors are shown in Table 4.2.

radio +.2. Computed moments C_1 , C_2 , C_3 , C_4 and C_3											
C,	'Ci	C_i	$\mu_{0(ci)}$								
C ₁	t10, t16	t9, t15									
C ₂	t8, t18	t7, t17									
C_3	t9, t17	t8, t16									
\rm{C}_4	t4, t13	t3, t12									
C_{5}	t13										

Table 4.2. Computed monitors C_1 , C_2 , C_3 , C_4 and C_5 .

Step 2.3.4: Redundancy test carried out shows that all five computed monitors are necessary.

Step 2.3.5: The controlled $PNM_3 := PNM_3 + C_1 + C_2 + C_3 + C_4 + C_5$ is shown in Fig. 4.6. It is live with 632 good states. This is the optimal live behaviour for the controlled PNM₃.

Figure 4.6. The controlled PNM_3 : = $PNM_3 + C_1 + C_2 + C_3 + C_4 + C_5$.

 $B:= B ++(B = 4).$

Step 2.4.1: $(B = 4)$, the PNM₄, shown in Fig. 4.7, is not live. The reachability graph RG₄ computed for the PNM₄ has 2,104 good states in the LZ₄ and 2 bad states BM₆ and BM⁷ within the DZ4.

Figure 4.7. The uncontrolled PNM4.

Step 2.4.2: The markings of the activity places of BM_6 and BM_7 are shown in Table 4.3.

	rable ± 0.5 . The markings of the activity of Divisional Division															
State		D	D	D	D	P	P	P	D	D	D	D	D	D	D	
nr.					$\overline{ }$	8	Q	ΙV				⊥ັ		$\overline{}$		
729																
38 ^c						υ	υ									

Table 4.3. The markings of the activity of BM_6 and BM_7 .

The place invariants PI_6 and PI_7 for BM_6 and BM_7 are:

 $PI_6 = \mu_{12} + \mu_{13} + \mu_{18} + \mu_{19} \leq 3$

$$
PI_7 = \mu_{11} + \mu_{18} \leq 3
$$

Step 2.4.3: The computation of monitors C_6 and C_7 are carried out as follows:

 p12 p13 p18 p19 $L_{P16} = [1 \ 1 \ 1 \ 1]$ t8 t9 t10 t15 t16 t17 $D_{PI6} =$ 1 0 ̵1 1 0 ̵1 0 0 0 0 0 0 0 $\boldsymbol{0}$ 0 0 0 0 $0 \t1 \t-1$ $1 -1 0$] p12 p13 p18 p19 $D_{C6} = -L_{P16}$. $D_{P16} = -[1 \ 1 \ 1 \ 1 \ 1]$ 1 0 ̵1 1 0 ̵1 0 0 0 0 0 0 0 0 0 0 0 0 $0 \t1 \t-1$ $1 -1 0$ $D_{C6} = - [1 \ 0 \ -1 \ 1 \ 0 \ -1]$ t8 t9 t10 t15 t16 t17 Therefore, $D_{C6} = [-1 \ 0 \ 1 \ -1 \ 0 \ 1]$ $\mu_{0(c6)} = 3$ p11 p18 $L_{PI7} = [1 \ 1]$ t7 t8 t16 t17 $D_{PT} = \begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & 1 & 1 \end{bmatrix}$ $\begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & 1 & -1 \end{bmatrix} \begin{bmatrix} p11 \\ p18 \end{bmatrix}$ $D_{C7} = -L_{P17}$ $D_{P17} = -[1 \quad 1] \begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & 1 & 1 \end{bmatrix}$ $\begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & 1 & -1 \end{bmatrix}$ D_{C7} = - [1 -1 1 -1] t7 t8 t16 t17 Therefore, $D_{C7} = [-1 \ 1 \ -1 \ 1]$ $\mu_{0(c7)} = 3$

The computed monitors are shown in Table 4.4.

]

		$\mu_{0(ci)}$
t10, t17	t8, t15	
t8, t17	t7, t16	

Table 4.4. Computed monitors C_6 and C_7

Step 2.4.4: Redundancy test carried out shows that the two computed monitors are necessary.

Step 2.4.5: The controlled $PNM_4 = PNM_4 + C_6 + C_7$ is shown in Fig. 4.8. It is live with 2,104 good states. This is the optimal live behaviour for the controlled PNM4.

Figure 4.8. The controlled $PNM_4 := PNM_4 + C_6 + C_7$.

 $B:= B ++(B = 4).$

Step 2.5.1: $(B = 5)$, the PNM₅, shown in Fig. 4.9, is not live. The reachability graph RG₅ computed for the PNM₅ has 5,190 good states in the LZ₅ and 2 bad states BM₈ and BM_9 within the DZ_5 .

Figure 4.9. The uncontrolled model PNM5.

Step 2.5.2: The markings of the activity places of BM₈ and BM₉ are shown in Table 4.5.

						\circ						\sim \sim		\sim		
State	p	\mathbf{p}	p	p	p		p	p	p	p			p			
nr.	\sim ∸	\sim ت		O	$\overline{ }$		9	10		1^{\wedge} $\overline{1}$	\sim 1 J	$\overline{}$ 12	10	$1 -$	18	19
1335		$\boldsymbol{0}$	$\overline{0}$	0			υ	ν								
1334	υ	ν	υ	ν			v									

Table 4.5. The markings of the activity places of BM_8 and BM_9 .

The place invariants PI₈ and PI₉ for BM₈ and BM₉ respectively are:

 $PI_8 = \mu_{11} + \mu_{12} + \mu_{18} + \mu_{19} \leq 4$

$$
PI_9 = \mu_{11} + \mu_{13} + \mu_{18} + \mu_{19} \le 4
$$

Step 2.5.3: The computation of the monitors C_8 and C_9 are carried out as follows:

 p11 p12 p18 p19 $L_{PI8} = [1 \ 1 \ 1 \ 1]$ t7 t8 t9 t15 t16 t17 $D_{PI8} = \vert$ 1 0 ̵1 1 0 ̵1 0 0 0 0 0 0 0 $\boldsymbol{0}$ 0 0 0 0 $0 \t1 \t-1$ $1 -1 0$] p11 p12 p18 p19 $D_{C8} = -L_{PI8}$. $D_{PI8} = -[1 \ 1 \ 1 \ 1]$ 1 0 ̵1 1 $\boldsymbol{0}$ $\mathbf 1$ 0 0 0 0 0 0 0 0 0 0 $\boldsymbol{0}$ 0 $0 \t1 \t-1$ $1 -1 0$] $D_{C8} = - [1 \ 0 \ -1 \ 1 \ 0 \ -1]$ t7 t8 t9 t15 t16 t17 Therefore, $D_{C8} = [-1 \ 0 \ 1 \ -1 \ 0 \ 1]$ $\mu_{0(c8)} = 4$ p11 p13 p18 p19 $L_{PI9} = \begin{bmatrix} 1 & 1 & 1 & 1 \end{bmatrix}$ t7 t8 t9 t10 t15 t16 t17 $D_{PP} =$ 1 0 ̵1 0 0 0 0 0 0 $1 -1 0 0 0$ $\boldsymbol{0}$ 0 0 0 $0 \t 0 \t 0 \t 1 \t -1$ $0 \t 0 \t 1 \t -1 \t 0$] p11 p13 p18 p19 D_{C9} = - L_{PI9} *. D_{PI9}* = - [1 1 1 1] 1 $\boldsymbol{0}$ ̵1 0 0 0 0 0 0 $1 -1 0 0 0$ 0 $\boldsymbol{0}$ 0 0 $0 \t 0 \t 1 \t -1$ $0 \t 0 \t 1 \t -1 \t 0$ $D_{C9} = -\begin{bmatrix} 1 & -1 & 1 & -1 & 1 & 0 & -1 \end{bmatrix}$ t7 t8 t9 t10 t15 t16 t17 Therefore, $D_{C9} = \begin{bmatrix} -1 & 1 & -1 & 1 & -1 & 0 & 1 \end{bmatrix}$ $\mu_{0(c9)} = 4$

]

The computed monitors are shown in Table 4.6.

1 able ± 0.0 . Computed moments C_8 and C_9 .												
			$\mu_{0(ci)}$									
	t9, t17	t7, t15										
	t8, t10, t17	t7, t9, t15										

Table 4.6. Computed monitors C_8 and C_9

Step 2.4.4: Redundancy test carried out shows that the two computed monitors are necessary. Step 2.4.5: The controlled $PNM_5 = PNM_5 + C_8 + C_9$ is shown in Fig. 4.10. It is live with 5,190 good states. This is the optimal live behaviour for the controlled PNM5.

Figure 4.10. The controlled PNM_5 := $PNM_5 + C_8 + C_9$.

 $B:= B ++(B = 6).$

Step 2.6.1: ($B = 6$), The PNM₆, shown in Fig. 4.11, is not live. The reachability graph RG₆ computed for the PNM $_6$ has 9,878 good states in the LZ $_6$ and 10 bad markings

 BM_{10} , BM_{11} , BM_{12} , BM_{13} , BM_{14} , BM_{15} , BM_{16} , BM_{17} , BM_{18} and BM_{19} within the DZ₆.

Figure 4.11. The PNM_6 .

Step 2.6.2: The markings of the activity places of BM_{10} , ..., BM_{19} of the PMM_6 are shown in Table 4.7.

							O			י ב			\sim 7 .	.,		
State nr.	p $\overline{2}$	p 3	p $\overline{4}$	p 6	p 7	p 8	p 9	p 10	p 11	p 12	p 13	p 15	p 16	p 17	p 18	p 19
1141	θ		θ	$\overline{0}$	$\boldsymbol{0}$			θ	$\overline{0}$	θ	$\overline{0}$	θ	$\overline{0}$	θ	2	
1148	θ	θ	$\overline{0}$	$\overline{0}$	$\boldsymbol{0}$	1	$\overline{2}$	$\overline{0}$	$\overline{0}$	θ	$\overline{0}$	θ	$\overline{0}$	$\overline{0}$	2	
1661	θ	Ω	$\overline{0}$	1	2	θ	Ω	$\overline{0}$	1	0	Ω	θ			$\overline{0}$	θ
1955	Ω	Ω	θ	$\overline{0}$	$\overline{0}$	1	$\overline{2}$	$\overline{0}$	$\overline{0}$	θ		θ	$\overline{0}$	$\overline{0}$		
2027	θ		θ	$\overline{0}$	$\overline{0}$			$\overline{0}$	$\overline{0}$	0		θ	$\overline{0}$	$\overline{0}$		
2570	Ω	Ω	θ	1	$\overline{2}$	$\overline{0}$	Ω	$\overline{0}$	$\overline{0}$	0	0	θ	$\overline{2}$		0	$\overline{0}$
3273			θ	$\overline{0}$	θ	$\overline{0}$		$\overline{0}$	$\overline{0}$	θ		θ	θ	$\overline{0}$		
3816		θ	θ	$\overline{0}$	$\overline{0}$	$\overline{0}$	$\overline{2}$	$\overline{0}$	$\overline{0}$	θ		θ	$\overline{0}$	$\overline{0}$		
3974			$\overline{0}$	$\overline{0}$	$\overline{0}$	$\overline{0}$		$\overline{0}$	$\overline{0}$	0	0	θ	$\overline{0}$	$\overline{0}$	$\overline{2}$	
9689		0	$\boldsymbol{0}$	$\overline{0}$	0	θ	2	$\overline{0}$	$\overline{0}$	0	θ	θ	θ	$\boldsymbol{0}$	2	

Table 4.7. The markings of the activity places of BM_{10}, \ldots, BM_{19} .

The place invariants PIs for the BMs respectively are:

 $PI_{10} = \mu_3 + \mu_8 + \mu_9 + \mu_{18} + \mu_{19} \leq 5$

$$
PI11 = \mu8 + \mu9 + \mu18 + \mu19 ≤ 5
$$

\n
$$
PI12 = \mu6 + \mu7 + \mu11 + \mu16 + \mu17 ≤ 5
$$

\n
$$
PI13 = \mu8 + \mu9 + \mu13 + \mu18 + \mu19 ≤ 5
$$

\n
$$
PI14 = \mu3 + \mu8 + \mu9 + \mu13 + \mu18 + \mu19 ≤ 5
$$

\n
$$
PI15 = \mu6 + \mu7 + \mu16 + \mu17 ≤ 5
$$

\n
$$
PI16 = \mu2 + \mu3 + \mu9 + \mu13 + \mu18 + \mu19 ≤ 5
$$

\n
$$
PI17 = \mu2 + \mu9 + \mu13 + \mu18 + \mu19 ≤ 5
$$

\n
$$
PI19 = \mu2 + \mu9 + \mu18 + \mu19 ≤ 5
$$

Step 2.6.3: The computation of the monitors
$$
C_{10}
$$
, C_{11} , C_{12} , C_{13} , C_{14} , C_{15} , C_{16} , C_{17} , C_{18} and C_{19} are carried out as follows:

 p3 p8 p9 p18 p19 $L_{PII0} = [1 \quad 1 \quad 1 \quad 1 \quad 1]$

 t3 t4 t5 t12 t13 t15 t16 t17 Therefore, $D_{CIO} = [-1 \ 0 \ 1 \ -1 \ 1 \ -1 \ 0 \ 1]$

 $\mu_{0(c10)} = 5.$

 p8 p9 p18 p19 $L_{PIII} = \begin{bmatrix} 1 & 1 & 1 & 1 \end{bmatrix}$ t3 t4 t5 t15 t16 t17 $D_{PIII} =$ 1 0 ̵1 0 0 0 0 $1 -1 0 0 0$ 0 0 $0 \t 0 \t 1 \t -1$ $0 \t 0 \t 1 \t -1 \t 0$] p8 p9 p18 p19 $D_{C11} = -L_{P111} \cdot D_{P111} = -[1 \ 1 \ 1 \ 1]$ 1 0 ̵1 0 0 0 0 $1 -1 0 0 0$ 0 0 $0 \t 0 \t 0 \t 1 \t -1$ $0 \t 0 \t 1 \t -1 \t 0$] D_{C11} = $-[1 \ 0 \ -1 \ 1 \ 0 \ -1]$ t3 t4 t5 t15 t16 t17 Therefore, $D_{C11} = \begin{bmatrix} -1 & 0 & 1 & -1 & 0 & 1 \end{bmatrix}$ $\mu_{0(c11)} = 5.$ p6 p7 p11 p16 p17 $L_{PII2} = [1 \ 1 \ 1 \ 1 \ 1]$ t1 t2 t3 t7 t8 t17 t18 t19 *DPI12 =* \lfloor ł I I I 1 0 $1 \t0 \t-1 \t0 \t0 \t0 \t0$ 1 ̵1 0 0 0 0 0 0 0 $\boldsymbol{0}$ $0 \t 0 \t 1 \t -1 \t 0 \t 0 \t 0$ 0 0 0 0 0 1 ̵1 $0 \t0 \t0 \t0 \t1 -1 \t0$ $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$ p6 p7 p11 p16 p17 $D_{C12} = -L_{P112}$. $D_{P112} = -[1 \ 1 \ 1 \ 1 \ 1]$ \lfloor I I I I 1 0 ̵1 0 ̵1 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 $0 \t 0 \t 1 \t -1 \t 0 \t 0 \t 0$ 0 0 0 0 0 1 ̵1 $0 \t0 \t0 \t0 \t1 -1 \t0$ $D_{C12} = - [1 \ 0 \ -1 \ 0 \ -1 \ 1 \ 0 \ -1]$ t3 t4 t5 t12 t13 t15 t16 t17 Therefore, $D_{C12} = \begin{bmatrix} -1 & 0 & 1 & 0 & 1 & -1 & 0 & 1 \end{bmatrix}$

 l $\overline{}$ $\overline{}$ $\overline{}$ $\mu_{0(c12)} = 5.$

 p8 p9 p13 p18 p19 $L_{PI13} = \begin{bmatrix} 1 & 1 & 1 & 1 & 1 \end{bmatrix}$ t3 t4 t5 t9 t10 t15 t16 t17 *DPI13 =* \lfloor ł I I I 1 0 ̵1 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 $\boldsymbol{0}$ $0 \t 0 \t 1 \t -1 \t 0 \t 0 \t 0$ 0 0 0 0 0 1 ̵1 $0 \t0 \t0 \t0 \t1 -1 \t0$ $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$ p8 p9 p13 p18 p19 $D_{C13} = -L_{P113} \cdot D_{P113} = -[1 \quad 1 \quad 1 \quad 1]$ [I I I I 1 0 ̵1 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 $\boldsymbol{0}$ $0 \t 0 \t 1 \t -1 \t 0 \t 0 \t 0$ 0 0 0 0 0 1 ̵1 $0 \t0 \t0 \t0 \t1 -1 \t0$ $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$ $D_{C13} = - [1 \ 0 \ -1 \ 1 \ -1 \ 1 \ 0 \ -1]$ t3 t4 t5 t9 t10 t15 t16 t17 Therefore, $D_{C13} = \begin{bmatrix} -1 & 0 & 1 & -1 & 1 & -1 & 0 & 1 \end{bmatrix}$ $\mu_{0(c13)} = 5.$ p3 p8 p9 p13 p18 p19 $L_{PII4} = [1 \ 1 \ 1 \ 1 \ 1 \ 1]$ t3 t4 t5 t9 t10 t11 t12 t15 t16 t17 *DPI14 =* \lfloor I I I I $\begin{bmatrix} 0 & 0 & 0 & 0 & 0 & 1 & -1 & 0 & 0 & 0 \\ 1 & -1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 &$ 1 -1 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 $0 \t0 \t0 \t0 \t0 \t0 \t0 \t1 -1 \t0$ l l $\overline{}$ $\overline{}$ $\overline{}$ p2 p8 p9 p13 p18 p19

 $D_{C14} = -L_{PII4}$. $D_{PII4} = -[1 \ 1 \ 1 \ 1 \ 1]$ \lfloor I I I I $\begin{bmatrix} 0 & 0 & 0 & 0 & 0 & 1 & -1 & 0 & 0 & 0 \\ 1 & -1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 &$ 1 -1 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 $0 \t0 \t0 \t0 \t0 \t0 \t0 \t1 -1 \t0$ $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$ D_{C14} = $-[1 \ 0 \ -1 \ 1 \ -1 \ 1 \ -1 \ 1 \ 0 \ -1]$ t3 t4 t5 t9 t10 t11 t12 t15 t16 t17 Therefore, $D_{C14} = \begin{bmatrix} -1 & 0 & 1 & -1 & 1 & -1 & 1 & -1 & 0 & 1 \end{bmatrix}$ $\mu_{0(c14)} = 5.$ p6 p7 p16 p17 $L_{PI15} = [1 \ 1 \ 1 \ 1]$ t1 t2 t3 t7 t17 t18 t19 *DPI15 =* [1 $\boldsymbol{0}$ $1 \t0 \t-1 \t0 \t0 \t0$ $1 -1 0 0 0 0$ $\boldsymbol{0}$ $\boldsymbol{0}$ $0 \t 0 \t 0 \t 1 \t -1$ $0 \t 0 \t 1 \t -1 \t 0$] p6 p7 p16 p17 $D_{C15} = -L_{P115}$. $D_{P115} = -[1 \ 1 \ 1]$ 1 0 $1 \t 0 \t -1 \t 0 \t 0 \t 0$ $1 -1 0 0 0 0$ $\begin{bmatrix} 0 & 1 & -1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & -1 \end{bmatrix}$ 0 0 0 0 1 ̵1 0 D_{C15} = -[1 0 -1 -1 1 0 -1] t1 t2 t3 t7 t17 t18 t19 Therefore, $D_{C15} = \begin{bmatrix} -1 & 0 & 1 & 1 & -1 & 0 & 1 \end{bmatrix}$ $\mu_{0(c15)} = 5.$ p2 p3 p9 p13 p18 p19 $L_{PI16} = \begin{bmatrix} 1 & 1 & 1 & 1 & 1 & 1 \end{bmatrix}$

 t4 t5 t9 t10 t11 t12 t13 t15 t16 t17 *DPI16 =* [0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 1 ̵1 0] p2 p3 p9 p13 p18 p19 *DC16* = ̵*LPI16 . DPI16* = ̵[1 1 1 1 1 1] [0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 1 ̵1 0] *DC16* = ̵ [1 ̵1 1 ̵1 1 0 ̵1 1 0 ̵1] t4 t5 t9 t10 t11 t12 t13 t15 t16 t17 Therefore, *DC16* = [̵1 1 ̵1 1 ̵1 0 1 ̵1 0 1] µ0(*c16*) = 5. p2 p9 p13 p18 p19 *LPI17* = [1 1 1 1 1] t4 t5 t9 t10 t11 t12 t15 t16 t17 *DPI17 =* [0 1 0 0 0 1 ̵1 0 0 0 ̵1 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 1 ̵1 0] p2 p9 p13 p18 p19 *DC17* = ̵ *LPI17 . DPI17* = ̵ [1 1 1 1 1] [0 1 0 0 0 1 ̵1 0 0 0 ̵1 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 1 ̵1 0] *DC17* = ̵ [1 ̵1 1 ̵1 1 ̵1 1 0 ̵1]

$$
t4 \t6 \t6 \t10 \t11 \t12 \t15 \t16 \t17
$$
\nTherefore, $D_{CI7} = [-111 - 111 - 111 - 1111 -$

$$
D_{C19} = - L_{PI19}. \ D_{PI19} = -\begin{bmatrix} 1 & 1 & 1 & 1 \end{bmatrix} \begin{bmatrix} 0 & 0 & 1 & -1 & 0 & 0 & 0 \\ 1 & -1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & -1 \\ 0 & 0 & 0 & 0 & 1 & -1 & 0 \end{bmatrix}
$$

 $D_{C19} = - \begin{bmatrix} 1 & -1 & 1 & -1 & 1 & 0 & -1 \end{bmatrix}$

 t4 t5 t11 t12 t15 t16 t17 Therefore, $D_{C19} = \begin{bmatrix} -1 & 1 & -1 & 1 & -1 & 0 & 1 \end{bmatrix}$

 $\mu_{0(c19)} = 5.$

Step 2.6.4: Redundancy test carried out shows that only three of the computed monitors are necessary, C12, C¹⁴ and C16. The remaining seven are redundant and are therefore removed. The necessary monitors are also renumbered in order to follow the regular sequence of numbering for convenience. Thus C_{11} becomes C_{10} , C_{13} becomes C_{14} and C_{16} becomes C_{12} .

The necessary monitors C_{10} , C_{11} , and C_{12} are shown in Table 4.8.

			$\mu_{0(ci)}$
C_{10}	t3, t8, t19	t1, t17	
\mathbf{C}_{11}	t5, t10, t13, t17	t3, t9, t12, t15	
$\frac{1}{2}$ 12	t5, t10, t13, t17	t4, t9, t11, t15	

Table 4.8. Necessary monitors C_{10} , C_{11} and C_{12} .

Step 2.6.5: The controlled PNM₆ := PNM₆ + C₁₀ + C₁₁ + C₁₂ is shown in Fig. 4.12. It is live with 9,878 good states. This is the optimal live behaviour for the controlled PNM₆.

Figure 4.12. The controlled PNM_6 : = $PNM_6 + C_{10} + C_{11} + C_{12}$.

 $B:= B ++(B = 7).$

Step 2.7.1: $(B = 7)$, The PNM₇, shown in Figure 4.13, is not live. The reachability graph RG₇ computed for the PNM₇ has 15,013 good states in the LZ₇ and 4 bad states BM₁₃, $BM₁₄, BM₁₅$ and BM₁₆ within the DZ₇.

Figure 4.13. The PNM7.

Step 2.7.2: The markings of the activity places of BM_{13} , ..., BM_{16} are shown in Table 4.9.

						ັ			\sim 1			\sim	\sim		
State	D	p		n	Ŋ	n	_D	D	n	n	n	n	n	n	
nr.	↩	⌒		O	O Õ	о	v		◠	13	5 ⊥⊃	.6	$\overline{}$	8	9
168	0	υ				⌒		~			U		.,		
896	υ		ν					~			U				
3116		υ	ν		v	⌒	U	↩							
4337								∼							

Table 4.9. The markings of the activity places of BM_{13}, \ldots, BM_{16} .

The place invariants PI_{13} , PI_{14} , PI_{15} and PI_{16} for BM_{13} , BM_{14} , BM_{15} and BM_{16} respectively are:

 $PI_{13} = \mu_8 + \mu_9 + \mu_{11} + \mu_{18} + \mu_{19} \leq 6$ $PI_{14} = \mu_3 + \mu_8 + \mu_9 + \mu_{11} + \mu_{18} + \mu_{19} \leq 6$ $PI_{15} = \mu_2 + \mu_9 + \mu_{11} + \mu_{18} + \mu_{19} \leq 6$ $PI_{16} = \mu_2 + \mu_3 + \mu_9 + \mu_{11} + \mu_{18} + \mu_{19} \leq 6$

Step 2.7.3: The computation of the monitors C_{13} , C_{14} , C_{15} and C_{16} are carried out as follows:

 p8 p9 p11 p18 p19 $L_{PII3} = \begin{bmatrix} 1 & 1 & 1 & 1 & 1 \end{bmatrix}$ t3 t4 t5 t7 t8 t15 t16 t17 $D_{PI13} =$ \lfloor I I I I 1 0 ̵1 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 1 ̵1 $0 \t0 \t0 \t0 \t1 -1 \t0$ l $\overline{}$ l $\overline{}$ p8 p9 p11 p18 p19 $D_{C13} = -L_{PII3} \cdot D_{PII3} = -[1 \quad 1 \quad 1 \quad 1]$ \lfloor I I I I 1 0 ̵1 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 1 -1 0 \mathbf{I} $\overline{}$ l l $D_{C13} = -[1 \ 0 \ -1 \ 1 \ -1 \ 1 \ 0 \ -1]$ t3 t4 t5 t7 t8 t15 t16 t17 Therefore, $D_{C13} = [-1 \ 0 \ 1 \ -1 \ 1 \ -1 \ 0 \ 1]$ $\mu_{0(c13)} = 6$

 p3 p8 p9 p11 p18 p19 *LPI14* = [1 1 1 1 1 1] t3 t4 t5 t7 t8 t12 t13 t15 t16 t17 *DPI*¹⁴ = [0 1 0 0 0 0 1 ̵1 0 0 0 ̵1 0 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 1 ̵1 0] p3 p8 p9 p11 p18 p19 *DC*¹⁴ = ̵*LPI14 . DPI*¹⁴ = [1 1 1 1 1 1] [0 1 0 0 0 0 1 ̵1 0 0 0 ̵1 0 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 1 ̵1 0] *DC*¹⁴ = ̵[1 0 ̵1 1 ̵1 1 ̵1 1 0 ̵1] t3 t4 t5 t7 t8 t12 t13 t15 t16 t17 Therefore, *DC*¹⁴ = [̵1 0 1 ̵1 1 ̵1 1 ̵1 0 1] µ0(*c*14) = 6 p2 p9 p11 p18 p19 *LPI15* = [1 1 1 1 1] t4 t5 t7 t8 t11 t12 t15 t16 t17 *DPI15 =* [0 0 0 0 1 ̵1 0 0 0 1 0 0 0 ̵1 0 0 0 0 1 0 0 0 ̵1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 1 ̵1 0] p2 p9 p11 p18 p19 *DC15* = ̵ *LPI15 . DPI*15 = ̵[1 1 1 1 1] [0 0 0 0 1 ̵1 0 0 0 1 0 0 0 ̵1 0 0 0 0 1 0 0 0 ̵1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 1 ̵1 0]

 $D_{CIS} = - \begin{bmatrix} 1 & -1 & 1 & -1 & 1 & -1 & 1 & 0 & -1 \end{bmatrix}$ t4 t5 t7 t8 t11 t12 t15 t16 t17 Therefore, $D_{C15} = \begin{bmatrix} -1 & 1 & -1 & 1 & -1 & 1 & -1 & 0 & 1 \end{bmatrix}$ $\mu_{0(c15)} = 6$ p2 p3 p9 p11 p18 p19 $L_{PII6} = \begin{bmatrix} 1 & 1 & 1 & 1 & 1 & 1 \end{bmatrix}$ t4 t5 t7 t8 t11 t12 t13 t15 t16 t17 *DC16 ⁼* [I I I I I 0 0 0 0 1 ̵1 0 0 0 0 0 1 0 0 0 0 ̵1 0 0 0 0 0 1 0 0 0 $\boldsymbol{0}$ ̵1 0 $\boldsymbol{0}$ 0 0 0 0 0 1 0 0 0 0 ̵1 0 0 0 0 $\boldsymbol{0}$ $\boldsymbol{0}$ $\boldsymbol{0}$ 0 1 0 0 0 1 ̵1 0 0 0 ̵1 $0₁$ $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$ p2 p3 p9 p11 p18 p19 $D_{C16} = -L_{P116}$. $D_{P116} = -[1 \ 1 \ 1 \ 1 \ 1]$ \lfloor I I I I I 0 0 0 0 1 ̵1 0 0 0 0 0 1 0 0 0 0 ̵1 0 0 0 0 0 1 0 0 0 0 ̵1 0 0 0 0 0 $\boldsymbol{0}$ 0 1 0 0 0 0 ̵1 0 0 0 0 $\boldsymbol{0}$ 0 0 $\boldsymbol{0}$ 1 0 0 0 1 ̵1 0 0 0 ̵1 $0¹$ \mathbf{I} l l l l $D_{C16} = -[1 \quad -1 \quad 1 \quad -1 \quad 1 \quad 0 \quad -1 \quad 1 \quad 0 \quad -1]$ t4 t5 t7 t8 t11 t12 t13 t15 t16 t17 Therefore, $D_{C16} = \begin{bmatrix} -1 & 1 & -1 & 1 & -1 & 0 & 1 & -1 & 0 & 1 \end{bmatrix}$ $\mu_{0(c16)} = 6$

Step 2.7.4: Redundancy test carried out shows that only three of the computed monitors are necessary, C_{14} , C_{15} and C_{16} while C_{13} is redundant and is therefore removed. The necessary monitors are also renumbered in order to follow the regular sequence of numbering for convenience. Thus C_{14} becomes C_{13} , C_{15} becomes C_{14} and C_{16} becomes C_{15} .

The computed necessary monitors C_{13} , C_{14} and C_{15} are shown in Table 4.10.

--------- -19, -17 ---- -19.			
C_i	$\cdot C_i$	C : 1	$\mu_{0(ci)}$
C_{13}	t5, t8, t13, t17	t3, t7, t12, t15	
C_{14}	t5, t8, t12, t17	t4, t7, t11, t15	
C_{15}	t5, t8, t13, t17	t4, t7, t11, t15	

Table 4.10. Necessary monitors C_{13} , C_{14} and C_{15} .

Step 2.7.5: The controlled $PNM_7 := PNM_7 + C_{13} + C_{14} + C_{15}$ is shown in Fig. 4.14. It is live with 15,013 good states. This is the optimal live behaviour for the controlled PNM7.

Figure 4.14. The controlled PNM_7 := PNM_7 + C_{13} + C_{14} + C_{15} .

 $B:= B ++(B = 8).$

Step 2.8.1: $(B = 8)$, when eight tokens are deposited in the GP, as shown in Fig. 4.15, the net PNM₈ is live with 18,972 good states. B: = B++ (B = 9).

Figure 4.15. The controlled PNM8.

Step 2.9.1: $(B = 9)$, when nine tokens are deposited in the GP, as shown in Fig. 4.16, the net PNM₉ is live with 20,980 good states. B: = B++ (B = 10).

Figure 4.16. The controlled PNM9.

Step 2.10.1: $(B = 10)$ The PNM₁₀, shown in Fig. 4.17, is not live. The reachability graph RG₁₀ computed for the PNM₁₀ has 21,536 good states in the LZ_{10} and 11 bad states

 BM_{16} , BM_{17} , BM_{18} , BM_{19} , BM_{20} , BM_{21} , BM_{22} , BM_{23} , BM_{24} , BM_{25} and BM_{26} within the DZ_{10} .

Figure 4.17. The PNM₁₀.

Step 2.7.2: The markings of the activity places of BM₁₆, ..., BM₂₆ are shown in Table 4.11.

						ັ				\sim 1			\cdot \cdot \cdot			
State	p	p	p	p	p	p	p	p	p	p	p	p	p	p	p	p
nr.	$\overline{2}$	3	4	6	⇁	8	9	10	11	12	13	15	16	17	18	19
273	0	0	0	1	$\overline{2}$	1	1	$\overline{0}$		$\overline{0}$	θ	θ	1	$\overline{0}$	$\overline{2}$	1
274	$\overline{0}$	$\overline{0}$	$\overline{0}$	1	$\overline{2}$	$\overline{0}$	2	$\overline{0}$		$\overline{0}$	$\overline{0}$	θ	1	$\overline{0}$	$\overline{2}$	
770	$\overline{0}$	$\overline{0}$	$\overline{0}$	1	$\overline{2}$	1	1	$\overline{0}$		$\overline{0}$	1	$\overline{0}$	1	$\overline{0}$		
771	0	0	0	1	$\overline{2}$	$\overline{0}$	2	$\overline{0}$		$\overline{0}$	1	$\boldsymbol{0}$				
893	$\overline{0}$	$\overline{0}$	$\overline{0}$	1	$\overline{2}$	1	1	$\overline{0}$	$\overline{0}$	$\overline{0}$	$\overline{0}$	0	$\overline{2}$	$\overline{0}$	$\overline{2}$	
894	0	0	0	1	$\overline{2}$	0	$\overline{2}$	0	0	$\overline{0}$	$\overline{0}$	θ	$\overline{2}$	$\overline{0}$	$\overline{2}$	
1431	$\overline{0}$	$\overline{0}$	$\overline{0}$	1	$\overline{2}$	$\overline{0}$	$\overline{2}$	$\overline{0}$	п	\mathbf{I}	$\overline{0}$	$\overline{0}$	1	$\overline{0}$		
1603	0	0	0	1	$\overline{2}$	θ	$\overline{2}$	0		$\overline{0}$	θ	θ	θ		$\overline{2}$	
4274	θ	θ	$\overline{0}$	1	$\overline{2}$	1	1	θ	θ	$\overline{0}$	-1	0	$\overline{2}$	$\overline{0}$		
4275	0	0	$\overline{0}$	1	$\overline{2}$	$\overline{0}$	$\overline{2}$	$\overline{0}$	$\overline{0}$	$\overline{0}$	1	$\overline{0}$	$\overline{2}$	$\overline{0}$		
12161	0	0	0		$\overline{2}$	0	2	0		$\overline{0}$		$\boldsymbol{0}$	0			

Table 4.11. The markings of the activity places of BM_{16}, \ldots, BM_{26} .

The place invariants PIs for the BMs respectively are:

PI₁₆ =
$$
\mu_6 + \mu_7 + \mu_8 + \mu_9 + \mu_{11} + \mu_{16} + \mu_{18} + \mu_{19} \le 9
$$

\nPI₁₇ = $\mu_6 + \mu_7 + \mu_9 + \mu_{11} + \mu_{16} + \mu_{18} + \mu_{19} \le 9$
\nPI₁₈ = $\mu_6 + \mu_7 + \mu_8 + \mu_9 + \mu_{11} + \mu_{13} + \mu_{16} + \mu_{18} + \mu_{19} \le 9$
\nPI₁₉ = $\mu_6 + \mu_7 + \mu_9 + \mu_{11} + \mu_{13} + \mu_{16} + \mu_{18} + \mu_{19} \le 9$
\nPI₂₀ = $\mu_6 + \mu_7 + \mu_8 + \mu_9 + \mu_{16} + \mu_{18} + \mu_{19} \le 9$
\nPI₂₁ = $\mu_6 + \mu_7 + \mu_9 + \mu_{16} + \mu_{18} + \mu_{19} \le 9$
\nPI₂₂ = $\mu_6 + \mu_7 + \mu_9 + \mu_{11} + \mu_{12} + \mu_{16} + \mu_{18} + \mu_{19} \le 9$
\nPI₂₃ = $\mu_6 + \mu_7 + \mu_9 + \mu_{11} + \mu_{17} + \mu_{18} + \mu_{19} \le 9$
\nPI₂₄ = $\mu_6 + \mu_7 + \mu_8 + \mu_9 + \mu_{13} + \mu_{16} + \mu_{18} + \mu_{19} \le 9$
\nPI₂₅ = $\mu_6 + \mu_7 + \mu_9 + \mu_{13} + \mu_{16} + \mu_{18} + \mu_{19} \le 9$
\nPI₂₆ = $\mu_6 + \mu_7 + \mu_9 + \mu_{11} + \mu_{13} + \mu_{17} + \mu_{18} + \mu_{19} \le 9$

Step 2.7.3: The computation of the monitors are carried out as follows:

 p6 p7 p8 p9 p11 p16 p18 p19 *L*_{PI16} = [1 1 1 1 1 1 1 1 1]

 t1 t2 t3 t4 t5 t7 t8 t15 t16 t17 t18 t19 *DPI16* = [1 0 ̵1 0 0 0 ̵1 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 1 0 0 0 ̵1 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 1 1 ̵1 ̵1 0 0 0 0 0] p6 p7 p8 p9 p11 p16 p18 p19 *DC16* = ̵ *LPI16 . DPI16* = ̵[1 1 1 1 1 1 1 1] [1 0 ̵1 0 0 0 ̵1 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 1 0 0 0 ̵1 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 1 1 ̵1 ̵1 0 0 0 0 0] *DC16* = ̵[1 0 0 0 ̵1 0 ̵1 1 0 ̵1 1 ̵1] t1 t2 t3 t4 t5 t7 t8 t15 t16 t17 t18 t19 Therefore, *DC16* = [̵1 0 0 0 1 0 1 ̵1 0 1 ̵1 1] µ0(*c16*) = 9 p6 p7 p9 p11 p16 p18 p19 *LPI17* = [1 1 1 1 1 1 1] t1 t2 t3 t4 t5 t7 t8 t15 t16 t17 t18 t19 *DPI17* = [1 0 ̵1 0 0 0 ̵1 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 1 ̵1 0 1 ̵1 0 0 1 ̵1 0 0 0] p6 p7 p9 p11 p16 p18 p19

DC17 = ̵*LPI17 . DPI17*

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= ̵[1 1 1 1 1 1 1] [̵1 0 0 0 0 ̵1 0 0 0 0 0 ̵1 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 1 ̵1 1 ̵1 0 0 ̵1 0 0 0] *DC17* = ̵[1 0 ̵1 1 ̵1 1 0 1 0 ̵1 1 ̵1] t1 t2 t3 t4 t5 t7 t8 t15 t16 t17 t18 t19 Therefore, *DC17* = [̵1 0 1 ̵1 1 ̵1 0 ̵1 0 1 ̵1 1] µ0(*c17*) = 9 p6 p7 p8 p9 p11 p13 p16 p18 p19 *LPI18* = [1 1 1 1 1 1 1 1 1] t1 t2 t3 t4 t5 t7 t8 t9 t10 t15 t16 t17 t18 t19 *DPI18* = [̵1 0 0 0 ̵1 0 0 0 0 0 0 0 0 ̵1 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 ̵1 0 0 0 0 0 0 ̵1 0 0 0 0 0 ̵1 ̵1 ̵1 0] p6 p7 p8 p9 p11 p13 p16 p18 p19 *DC18* = ̵*LPI18 . DPI18* = ̵[1 1 1 1 1 1 1 1 1] [̵1 0 0 0 ̵1 0 0 0 0 0 0 0 0 ̵1 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 ̵1 0 0 0 0 0 0 ̵1 0 0 0 0 0 ̵1 ̵1 *DC18* = ̵[1 0 0 0 ̵1 0 ̵1 1 ̵1 1 0 ̵1 1 ̵1]

̵1 $0₁$ $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$

 t1 t2 t3 t4 t5 t7 t8 t9 t10 t15 t16 t17 t18 t19 Therefore, $D_{C18} = \begin{bmatrix} -1 & 0 & 0 & 0 & 1 & 0 & 1 & -1 & 1 & -1 & 0 & 1 & -1 & 1 \end{bmatrix}$ $\mu_{0(c18)} = 9$

 p6 p7 p9 p11 p13 p16 p18 p19 *LPI19* = [1 1 1 1 1 1 1 1] t1 t2 t3 t4 t5 t7 t8 t9 t10 t15 t16 t17 t18 t19 *DPI19* = L_0 I I I I I I \int_0^1 $\begin{bmatrix} 1 & -1 & 0 & 0 & 0 & -1 & 0 \end{bmatrix}$ 1 0 0 0 -1 0 0 0 0 0 0 0 0 1 -1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 -1 0 0 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 1 -1 0 0 0 0 0 0 $\boldsymbol{0}$ 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 1 ̵1 $0 \t 0 \t 1 \t -1 \t 0 \t 0$ $0 \quad 0$ $\overline{}$ \mathbf{I} \mathbf{I} l $\overline{}$ $|$ p7 p6 p9 p11 p13 p16 p18 p19

DC19 = ̵*LPI19 . DPI19*

 $D_{C19} = -\begin{bmatrix} 1 & 0 & -1 & 1 & -1 & 1 & 0 & 1 & -1 & 1 & 0 & -1 & 1 & -1 \end{bmatrix}$

 t1 t2 t3 t4 t5 t7 t8 t9 t10 t15 t16 t17 t18 t19 Therefore, $D_{C19} = \begin{bmatrix} -1 & 0 & 1 & -1 & 1 & -1 & 0 & -1 & 1 & 1 \end{bmatrix}$

 $\mu_{0(c19)} = 9$

 p6 p7 p8 p9 p16 p18 p19 $L_{PI20} = \begin{bmatrix} 1 & 1 & 1 & 1 & 1 & 1 & 1 \end{bmatrix}$

 t1 t2 t3 t4 t5 t7 t15 t16 t17 t18 t19 *DPI20* = [1 0 ̵1 0 0 0 ̵1 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 1 ̵1 0 1 ̵1 0 1 ̵1 0 0 1 ̵1 0 0 0] p6 p7 p8 p9 p16 p18 p19

 $D_{C20} = -L_{PI20}$ *. D_{PI20}*

$$
= -\begin{bmatrix} 1 & 1 & 1 & 1 & 1 & 1 & 1 \end{bmatrix} \begin{bmatrix} 1 & -1 & 0 & 0 & 0 & -1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & -1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & -1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & -1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & -1 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & -1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & -1 & 0 & 0 \end{bmatrix}
$$

$$
D_{C20} = -\begin{bmatrix} 1 & 0 & 0 & 0 & -1 & -1 & 1 & 0 & -1 & 1 & -1 \end{bmatrix}
$$

 t1 t2 t3 t4 t5 t7 t15 t16 t17 t18 t19 Therefore, $D_{C20} = \begin{bmatrix} -1 & 0 & 0 & 0 & 1 & 1 & -1 & 0 & 1 & -1 & 1 \end{bmatrix}$

 $\mu_{0(20)} = 9$

 p6 p7 p9 p16 p18 p19 $L_{PI2I} = \begin{bmatrix} 1 & 1 & 1 & 1 & 1 & 1 \end{bmatrix}$ t1 t2 t3 t4 t5 t7 t15 t16 t17 t18 t19 $D_{PI21} =$ [I I I I I 1 0 1 0 0 0 -1 0 0 0 0 0 1 -1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 $\boldsymbol{0}$ 0 $\boldsymbol{0}$ $0 \t 0 \t 0 \t 1 \t -1$ $0 \t1 \t-1 \t0 \t0$ $1 -1 0 0 0$ $\overline{}$ l l l l p6 p7 p9 p16 p18 p19

DC21 = ̵ *LPI21 . DPI21*

= ̵[1 1 1 1 1 1] [̵1 0 0 0 ̵1 0 0 0 0 0 ̵1 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 1 ̵1 1 ̵1 0 0 ̵1 0 0 0] *DC21* = ̵[1 0 ̵1 1 ̵1 ̵1 1 0 ̵1 1 ̵1] t1 t2 t3 t4 t5 t7 t15 t16 t17 t18 t19 Therefore, *DC21* = [̵1 0 1 ̵1 1 1 ̵1 0 1 ̵1 1] µ0(*21)* = 9 p6 p7 p9 p11 p12 p16 p18 p19 *LPI22* = [1 1 1 1 1 1 1 1] t1 t2 t3 t4 t5 t7 t8 t9 t15 t16 t17 t18 t19 *DPI22* = [̵1 0 0 0 ̵1 0 0 0 0 0 0 0 ̵1 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 ̵1 0 0 0 0 0 0 0 0 1 ̵1 ̵1 ̵1 0 0 0 0] p6 p7 p9 p11 p12 p16 p18 p19 *DC22* = ̵ *LPI22 . DPI22*

$$
= -\begin{bmatrix} 1 & 1 & 1 & 1 & 1 & 1 & 1 \end{bmatrix} \begin{bmatrix} 1 & -1 & 0 & 0 & 0 & -1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & -1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & -1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & -1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & -1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & -1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & -1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & -1 & 0 & 0 \end{bmatrix}
$$

 $D_{C22} = -\begin{bmatrix} 1 & 0 & -1 & 1 & -1 & 0 & 0 & -1 & 1 & 0 & -1 & 1 & -1 \end{bmatrix}$

 t1 t2 t3 t4 t5 t7 t8 t9 t15 t16 t17 t18 t19 Therefore, $D_{C22} = \begin{bmatrix} -1 & 0 & 1 & -1 & 1 & 0 & 0 & 1 & -1 & 0 & 1 & -1 & 1 \end{bmatrix}$ $\mu_{0(c22)} = 9$ p6 p7 p9 p11 p17 p18 p19

p6

 $L_{PI23} = \begin{bmatrix} 1 & 1 & 1 & 1 & 1 & 1 & 1 \end{bmatrix}$ t1 t2 t3 t4 t5 t7 t8 t15 t16 t17 t18 1 ̵1 0 0 0 ̵1 0 0 0 0 0

DC23 = ̵ *LPI23 . DPI23*

$$
D_{C23} = -[1 \quad 1 \quad 1 \quad 1 \quad 1 \quad 1 \quad 1] \begin{bmatrix} 1 & -1 & 0 & 0 & 0 & -1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & -1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & -1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & -1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & -1 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & -1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & -1 & 0 \end{bmatrix}
$$

\n
$$
D_{C23} = -[1 \quad 0 \quad -1 \quad 1 \quad -1 \quad 0 \quad -1 \quad 1 \quad 0 \quad 0 \quad -1]
$$

\n
$$
t1 \quad t2 \quad t3 \quad t4 \quad t5 \quad t7 \quad t8 \quad t15 \quad t16 \quad t17 \quad t18
$$

\nTherefore, $D_{C23} = [-1 \quad 0 \quad 1 \quad -1 \quad 1 \quad 0 \quad 1 \quad -1 \quad 0 \quad 0 \quad 1]$
\n
$$
\mu_{0(c23)} = 9
$$

 p6 p7 p8 p9 p13 p16 p18 p19 *LPI24* = $\begin{bmatrix} 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 \end{bmatrix}$

DC24 = ̵ *LPI24 . DPI24*

 $D_{C24} = - [1 \ 0 \ 0 \ 0 \ -1 \ -1 \ 1 \ -1 \ 1 \ 0 \ -1 \ 1 \ -1]$

 t1 t2 t3 t4 t5 t7 t9 t10 t15 t16 t17 t18 t19 Therefore, $D_{C24} = \begin{bmatrix} -1 & 0 & 0 & 0 & 1 & 1 & -1 & 1 & -1 & 0 & 1 & -1 & 1 \end{bmatrix}$

 $\mu_{0(c24)} = 9$

 p6 p7 p9 p13 p16 p18 p19 $L_{PI25} = \begin{bmatrix} 1 & 1 & 1 & 1 & 1 & 1 & 1 \end{bmatrix}$

 t1 t2 t3 t4 t5 t7 t9 t10 t15 t16 t17 t18 t19 $D_{PI25} =$ [I I I I I \int_0^1 $\begin{bmatrix} 1 & -1 & 0 & 0 & 0 & -1 \end{bmatrix}$ 1 0 0 0 -1 0 0 0 0 0 0 0 0 1 -1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 -1 0 $\boldsymbol{0}$ 0 0 $\boldsymbol{0}$ 1 ̵1 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 1 1 ̵1 ̵1 0 0 $0 \quad 0 \quad 0$ $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$ $0 \t 01$ $p6$ p7 p9 p13 p16 p18 p19

DC25 = ̵*LPI25 . DPI25*

$$
D_{C25} = -[1 \quad 1 \quad 1 \quad 1 \quad 1 \quad 1 \quad 1] \begin{bmatrix} 1 & -1 & 0 & 0 & 0 & -1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & -1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & -1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & -1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & -1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & -1 & 0 & 0 & 0 \end{bmatrix}
$$

\n
$$
D_{C25} = -[1 \quad 0 \quad -1 \quad 1 \quad -1 \quad -1 \quad 1 \quad -1 \quad 1 \quad 0 \quad -1 \quad 1 \quad -1 \quad 1 \quad 0 \quad 1 \quad -1 \quad 1]
$$

\n
$$
\mu_{0(c25)} = 9
$$

\n
$$
\mu_{0(c25)} = 9
$$

 p6 p7 p9 p11 p13 p17 p18 p19 *LPI26* = [1 1 1 1 1 1 1 1]

 $D_{C26} = -L_{PI26}$ *. D_{PI26}*

= ̵[1 1 1 1 1 1 1 1] [̵1 0 0 0 ̵1 0 0 0 0 0 0 0 ̵1 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 1 ̵1 0 0 0 0 0 0 0 0 1 ̵1 ̵1 0 ̵1 0 0] *DC26* = ̵ [1 0 ̵1 1 ̵1 0 ̵1 1 ̵1 1 0 0 ̵1]

 t1 t2 t3 t4 t5 t7 t8 t9 t10 t15 t16 t17 t18 Therefore, $D_{C26} = \begin{bmatrix} -1 & 0 & 1 & -1 & 1 & 0 & 1 & -1 & 1 & -1 & 0 & 0 & 1 \end{bmatrix}$ $\mu_{0(c26)} = 9$

Step 2.10.4: Redundancy test carried out shows that only three of the computed monitors are necessary, C_{18} , C_{22} and C_{26} while the rest of the monitors computed are redundant and are therefore removed. The necessary monitors are also renumbered in order to follow the regular sequence of numbering for convenience. Thus C18 becomes C_{16} , C_{22} becomes C_{17} and C_{26} becomes C_{18} .

The necessary monitors C_{16} , C_{17} and C_{18} are shown in Table 4.12.

	\cdot C:	C_i	$\mu_{0(ci)}$
C_{16}	t5, t8, t10, t17, t19	t1, t9, t15, t18	
C_{17}	t3, t5, t9, t17, t19	t1, t4, t15, t18	
C_{18}	t3, t5, t8, t10, t18	t1, t4, t9, t15	

Table 4.12. Necessary monitors C_{16} , C_{17} and C_{18} .

Step 2.10.5: The controlled $PNM_{10} := PNM_{10} + C_{16} + C_{17} + C_{18}$ is shown in Fig. 4.18. It is live with 21,513 good states. This is the optimal live behaviour for the controlled PNM10.

Figure 4.18. The controlled model PNM_{10} ($PNM_{10} := PNM_{10} + C_{16} + C_{17} + C_{18}$).

 $B := B ++(B = 11)$

Step 2.5.1: $(B = 11)$, the PNM₁₁ shown Fig. 4.19 is live with 21,562 good states.

Figure 4.19. The Live PNM₁₁.

Step 3: The design procedure applied in Step 2 is provided in Table 4.13. The computed eighteen necessary monitors are added in the uncontrolled PNM, the controlled PNM is obtained as shown in Fig. 4.20. It is verified that this controlled model is live with 21,562 good states.

Figure 4.20. The controlled PNM.

C_i	$\cdot C_i$	C_i^{\bullet}	$\mu_{0(Ci)}$
C_1	t10, t16	t9, t16	$\overline{2}$
C_2	t8, t18	t7, t17	\overline{c}
C_3	t9, t17	t8, t16	$\overline{2}$
C_4	t4, t13	t3, t12	$\overline{2}$
C_5	t13	t11	\overline{c}
C_6	t10, t17	t8, t15	$\overline{\mathbf{3}}$
C_7	t8, t17	t7, t16	$\overline{3}$
C_8	t9, t17	t7, t15	$\overline{4}$
C_9	t8, t10, t17	t7, t9, t15	$\overline{\mathcal{L}}$
C_{10}	t3, t8, t19	t1, t17	5
C_{11}	t5, t10, t13, t17	t3, t9, t12, t15	5
C_{12}	t5, t10, t13, t17	t4, t9, t11, t15	$\overline{5}$
C_{13}	t5, t8, t13, t17	t3, $t7$, $t12$, $t15$	6
C_{14}	t5, t8, t12, t17	t4, t7, t11, t15	6
C_{15}	t5, t8, t13, t17	t4, t7, t11, t15	6
C_{16}	t5, t8, t10, t17, t19	t1, t9, t15, t18	9
C_{17}	t3, t5, t9, t17, t19	t1, t4, t15, t18	9
C_{18}	$t3 t5$, $t8$, $t10$, $t18$	t1, t4, t9, t15	9

Table 4.13. Computed necessary monitors.

Table 4.14 shows the liveness enforcing procedure applied to the given PNM of Fig. 4.1. The maximally permissive behaviour of the PNM must provide 21,581 good states. Using our method, we obtained live behaviour with 21,562 good states, 99.91% of the maximally permissive behaviour.

		Is	$#$ of	# of	# of		#of states With in	
B	Included	the	States	States	States		Controlled	
						Computed		
	\mathcal{C}	net	in RG	in DZ	in LZ	C	net	
		live?					$RG =$	UR
							LZ	
$\mathbf{1}$		YES	17	$\overline{0}$	17		17	
$\overline{2}$		YES	132	θ	132		132	
3		N _O	637	5	632	$C_1, C_2, C_3, C_4,$	632	$\overline{0}$
						C_5		
$\overline{4}$	$C_1, C_2, , C_5$	NO	2,106	$\overline{2}$	2,104	C_6, C_7	2,104	$\overline{0}$
5	$C_1, C_2, , C_7$	NO	5,192	$\overline{2}$	5,190	C_8, C_9	5,190	$\overline{0}$
6	$C_1, C_2, , C_9$	NO	9,888	10	9,878	C_{10} , C_{11} , C_{12}	9,878	$\overline{0}$
						(7 redundant)		
$\overline{7}$	$C_1, C_2, , C_{12}$	N _O	15,017	$\overline{4}$	15,013	C_{13} , C_{14} , C_{15}	15,013	$\overline{0}$
						(1 redundant)		
8	$C_1, C_2, , C_{15}$	YES	18,972	$\overline{0}$	18,972		18,972	$\overline{0}$
9	$C_1, C_2, , C_{15}$	YES	20,980	Ω	20,980		20,980	Ω
10	$C_1, C_2, , C_{15}$	N _O	21,536	11	21,525	C_{16} , C_{17} , C_{18}	21,513	12
						(1 redundant)		
11	$C_1, C_2, , C_{18}$	YES	21,562	$\overline{0}$	21,562		21,562	19

Table 4.14. The liveness enforcing procedure applied to $S³PR PNM$.

4.2 AN AEMG PETRI NET EXAMPLE

The PNM of an FMS shown in Fig. 4.21 from [35], suffers from deadlocks. It has 3,136 states within the RG, 1,466 states of which are in the LZ while remaining 1,670 states are in the DZ.

Figure 4.21. An AEMG Petri net model of an FMS from [35].

There are twenty one activity places $P_A = \{p2-p4, p6-p18, p20-p24\}$, eleven shared resource places $P_R = \{p25-p27, p28-p35\}$, and three sink/source places $P_{S/S} = \{p1, p5, p19\}$. Table 4.15 gives the liveness enforcing procedure applied to the AEGM PNM.

Table 4.15 shows the liveness enforcing procedure applied to the AEMG PNM of Fig. 4.21. The optimal solution for this PNM must provide 1,466 reachable good states. By using our method, we obtained permissiveness of 1,330 reachable states which is 90.72% of the optimal solution. Table 4.16 provides the necessary computed monitors and their related arcs.

							# of states	
		Is the	# of	# of	$#$ of		Within	
B	Included	net	States	states	States	Computed	controlled net	
	\mathcal{C}	live?	in RG	in DZ	in LZ	C	$RG =$	UR
							LZ	
		YES	47	$\overline{0}$	17		17	
$\overline{2}$		NO	446	80	366	C_1, C_2, \ldots, C_{40}	366	θ
						(40 redundant)		
3	C_1, C_2, \ldots, C_{40}	N _O	991	$\overline{2}$	989	C_{41} , C_{42}	989	$\overline{0}$
$\overline{4}$	$C_1, C_2, , C_{42}$	N _O	1,305	$\overline{2}$	1,303	C_{43}	1,303	$\overline{0}$
						(1 redundant)		
$5\overline{)}$	C_1, C_2, \ldots, C_{43}	YES	1,329	$\overline{0}$	1,329		1,329	θ
6	C_1, C_2, \ldots, C_{43}	YES	1,330	Ω	1,330		1,330	θ

Table 4.15. The liveness enforcing procedure applied to AEGM PNM.

C_i	C_i	C_i [*]	$\mu_{0(Ci)}$	C_i	C_i	C_i [*]	$\mu_{0(Ci)}$
C_1	t3	t1	$\mathbf{1}$	C_{23}	t8, t17, t20	t6, t16, t19	$\overline{2}$
C ₂	t16, t18	t11, t12, t14	$\overline{3}$	C_{24}	t8, t16, t20	t6, t15, t19	$\overline{2}$
C_3	t14, t16, t18	t5, t11, t12, t15	3	C_{25}	t9, t17, t20	t7, t16, t19	$\overline{2}$
C_4	t15, t18	t5, t11, t12	$\overline{3}$	C_{26}	t9, t16, t20	t7, t15, t19	$\overline{2}$
C_5	t8, t11, t18	t6, t9, 1t6	$\overline{3}$	C_{27}	t6, t7, t17, t20	t5, t16, t19	$\overline{2}$
C_6	t11, t18	t7, t16	3	C_{28}	t6, t7, t16, t20	t5, t15, t19	$\overline{2}$
C ₇	t17, t18, t20	t12, t13, t16, t19	$\overline{2}$	C_{29}	t12, t15, t20	t10, t14, t19	$\overline{2}$
C_8	t18, t16, t20	t12, t13, t15, t19	$\overline{2}$	C_{30}	t10, t15, t20	t8, t14, t19	$\overline{2}$
C ₉	t18, t15, t20	t12, t13, t14, t19	$\overline{2}$	C_{31}	t6, t7, t15, t20	t5, t14, t19	$\overline{2}$
C_{10}	t18, t14, t20	t12, t13, t5, t19	$\overline{2}$	C_{32}	t8, t15, t20	t6, t14, t19	$\overline{2}$
C_{11}	t13, t16, t20	t11, t15, t19	$\overline{2}$	C_{33}	t6, t7, t11, t18	t5, t9, t16	3
C_{12}	t13, t15, t20	t11, t14, t19	$\overline{2}$	C_{34}	t12, t14, t20	t10, t5, t19	$\overline{2}$
C_{13}	t13, t14, t20	t11, t5, t19	$\overline{2}$	C_{35}	t10, t14, t20	t5, t8, t19	$\overline{2}$
C_{14}	t11, t17, t20	t9, t16, t19	$\overline{2}$	C_{36}	t8, t14, t20	t5, t6, t19	$\overline{2}$
C_{15}	t11, t16, t20	t9, t15, t19	$\overline{2}$	C_{37}	t6, t7, t14, t23	2t5, t22	$\mathbf{2}$
C_{16}	t11, t15, t20	t9, t14, t19	$\overline{2}$	C_{38}	t6, t7, t14, t22	2t5, t21	$\overline{2}$
C_{17}	t11, t14, t20	t9, t5, t19	$\overline{2}$	C_{39}	t6, t7, t14, t21	2t5, t20	$\overline{2}$
C_{18}	t9, t14, t20	t7, t5, t19	$\overline{2}$	C_{40}	t6, t7, t14, t20	2t5, t19	$\overline{2}$
C_{19}	t12, t17, t20	t10, t16, t19	$\overline{2}$	C_{41}	t2, t15, t18	t1, t5, t10, t13	$\overline{4}$
C_{20}	t12, t16, t20	t10, t15, t19	$\overline{2}$	C_{42}	t2, t12, t13, t15	t1, t5, t10, t11	$\overline{4}$
C_{21}	t10,t17, t20	t8, t16, t19	$\overline{2}$	C_{43}	t3, t7, t10, t18	t2, t5, t15	6
C_{22}	t10, t16, t20	t8, t15, t19	$\mathbf{2}$				

Table 4.16. Computed necessary monitors.

4.3 PERFORMANCE COMPARISON OF DIFFERENT CONTROL POLICIES

4.3.1 Performance Comparison for the S³PR Example

Table 4.17 shows the performance comparison of our solution on the S^3PR example with other different solutions previously provided on the $S³PR$ model in the literature.

Parameters	Ezpeleta	Li and	Huang	Li et al.	Uzam	Our		
	et al.	Zhou	et al.	[21]	and	solution		
	[16]	[8]	[20]		Zhou			
					[13]			
# of monitors added	18		12		19	18		
# of reachable states	6287	6287	12656	16636	21562	21562		
Permissiveness (%)	29.13	29.13	58.64	77.09	99.91	99.91		

Table 4.17 Performance comparison of different control policies for the S^3 PR model.

 The permissiveness of PNM increases as we move from the left to the right of the Table 4.17. Our solution and the one in [13] give the highest number of reachable states but with different number of monitors. In our solution, 18 monitors are required to obtain the number of reachable states we obtained while in [13] 19 monitors are required. The proposed method is straightforward. The only modification it requires to be made in the original PNM is the addition of the global sink/source place. Also a small number of control places is desirable in the design of a liveness enforcing supervisor as it reduces the structural complexity of a controlled PN model.

4.3.2 Performance Comparison for the AEMG Example

Table 4.18 shows the performance comparison of our solution on the AEGM example with the one provided in [35] for the AEMG example.

	acio "Alc'i chomano companion" of the component policies for the fill chi model.	
Parameters		Our solution
# of monitors added		
# of reachable states	167	1,330
Permissiveness (%)	139	

Table 4.18 Performance comparison of two control policies for the AEGM model.

 It can be seen from Table 4.18 that our method provides very high number of reachable states compared with that of [35]. Our solution provides near optimal permissiveness for the AEGM example.

CHAPTER 5

CONCLUSIONS

In this thesis a new method for the synthesis of Petri net based liveness enforcing supervisors in FMS is proposed. The method is simple, straightforward and easy to apply. The applicability of the proposed method to Petri net models suffering from deadlocks is demonstrated through examples. It does not require dividing the given PN model into subnets as in [29]. The only modification it requires in its algorithm is the addition of global sink/source place (GP) which is used temporarily in the computation stage, and it is removed when the net becomes live. The computation is carried out in an iterative way by increasing the number of tokens in the GP at each iteration.

The method provides very high behavioral permissiveness. It is not restricted to certain classes of PN models and it can be applied to many classes of Petri nets currently available in the literature.

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